SPECIFICATIONS FOR LCD MODULE

LCD Module Specification

CUSTOMER	
MODEL	SCT015002-V02
CUSTOMER APPROVED	

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RECORDS OF REVISIONS

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1. General Description

This Module SCT015002-V02 is TFT Liquid Crystal Display Module. This specification covers the delivery requirements for the liquid crystal display module delivered by quality to Customer.

1.1. Mechanical & Display Specifications

LCD Module Specification

Item	Standard value	Unit
LCD Size	1.54	inch
Dot Matrix	240(RGB) ×240	pixel
Module Size	31.52 ×33.72 ×1.73	mm
Active Area	27.72 × 27.72	mm
Dot Pitch	0.1155 × 0.1155	mm
Pixel Configuration	R.G.B. Stripe	-
Color depth	262K	-
Display Mode	Normally Black, Transmissive	-
Technology Type	a-Si	-
Viewing Direction	All	-
Gray Scale Inversion Direction	All	-
Driver IC	ST7789V2	-
Interface	4-line SPI	-
LED Numbers	3 LEDs	-
Weight	TBD	g



1.2. Interface Pin

Pin No.	Symbol	Type	Description
1	GND	P	Ground
2	CS	I	Chip Select signal
3	DC	I	Display data/command selection pin in 4-line serial interface
4	SDA	I/O	Serial data input/output pin
5	SCL	I	Serial clock pin
6	RESET	I	Reset signal
7	TE	О	Tearing effect output pin
8	LEDA	P	LED driving anode
9	LEDK	P	LED driving cathode
10	GND	P	Ground
11	NC	-	No connection
12	NC	-	No connection
13	NC	-	No connection
14	NC	-	No connection
15	GND	P	Ground
16	VDDI	P	Power supply for I/O system
17	VCC	P	Power supply for analog
18	GND	P	Ground
19	NC	-	No connection
20	NC	-	No connection
21	NC	-	No connection
22	NC	-	No connection
23	NC	-	No connection
24	GND	P	Ground

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2. Electrical Characteristics

2.1. Absolute Maximum Rating

LCD Module Specification

Item	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	VCC	-0.3	4.6	V	
Power Supply Voltage (Logic)	VDDI	-0.3	4.6		
Input Signal Voltage	$V_{\rm IN}$	-0.3	VDDI	V	Note 1
Operating Temperature	T_{OPR}	-20	+70	°C	Ambient
Storage Temperature	T_{STG}	-30	+80	°C	Ambient

Note1: VIN represent IO

2.2. DC Electrical Characteristics

2.2.1. Driving TFT LCD Panel

GND=0V, Ta=25°C

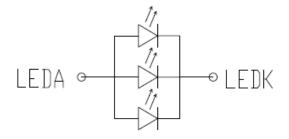
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Operating Voltage	VCC	2.4	2.75	3.3	V	
Interface Operating Voltage	VDDI	1.65	1.8	3.3		
Logic High level input voltage	V_{IH}	0.7VDDI	-	VDDI	V	
Logic Low level input voltage	V _{IL}	0	-	0.3VDDI	V	
Logic High level output voltage	V _{OH}	0.8VDDI	-	VDDI	V	I _{OH} =-1.0mA
Logic Low level output voltage	V _{OL}	0	-	0.2VDDI	V	I _{OL} =1.0mA
Power Consumption	I_{CC}	-	TBD	-	mA	

2.2.2. Driving Backlight

Ta=25°C

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Forward Current	I_{F}	-	60	60	mA	Note1
Forward Current Voltage	V_{F}	2.75	3.1	3.5	V	
Operating Life Time	-	10000			hrs	

Note 1: The figure below shows the connection of backlight LED.



Note 2: One LED: $I_F = 20 \text{mA}$.



2.3. AC Electrical Characteristics

2.3.1. 4-line Serial Interface Characteristics

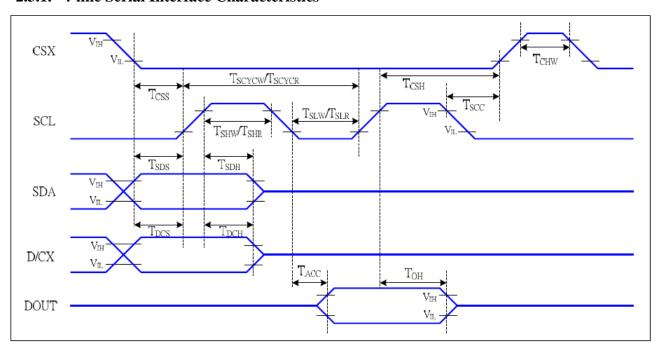


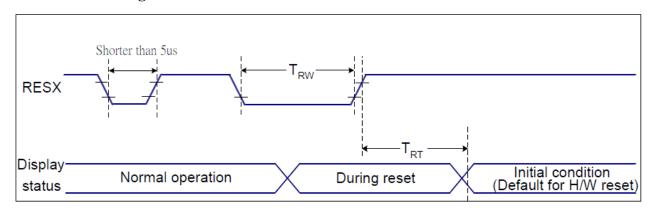
Figure 5 4-line serial Interface Timing Characteristics

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
	Tcss	Chip select setup time (write)	15		ns	
	T _{CSH}	Chip select hold time (write)	15		ns	
CSX T _{CSS}		Chip select setup time (read)	60		ns	
	T _{scc}	Chip select hold time (read)	65		ns	
	T _{CHW}	Chip select "H" pulse width	40		ns	
	Tscycw	Serial clock cycle (Write)	16		ns	-write command & data
	T _{SHW}	SCL "H" pulse width (Write)	7		ns	
SCL T _{SLW}		SCL "L" pulse width (Write)	7		ns	ram
SCL	Tscycr	Serial clock cycle (Read)	150		ns	wood commond 8 data
	T _{SHR}	SCL "H" pulse width (Read)	60		ns	-read command & data
T _{SLR}		SCL "L" pulse width (Read)	60		ns	Talli
D/CX	T _{DCS}	D/CX setup time	10		ns	
DICX	Тосн	D/CX hold time	10		ns	
SDA	T _{SDS}	Data setup time	7		ns	
(DIN)	T _{SDH}	Data hold time	7		ns	
DOUT	T _{ACC}	Access time	10	50	ns	For maximum CL=30pF
DOOT	Тон	Output disable time	15	50	ns	For minimum CL=8pF

Note: The rising time and falling time (Tr, Tf) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for Input signals.



2.3.2. Reset Timing



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Figure 7 Reset Timing

VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=25 $^{\circ}$

Related Pins	Symbol	Parameter	MIN	MAX	Unit
	TRW	Reset pulse duration	10	-	us
RESX	TDT	Paget cancel	-	5 (Note 1, 5)	ms
	TRT	Reset cancel		120 (Note 1, 6, 7)	ms

Table 9 Reset Timing

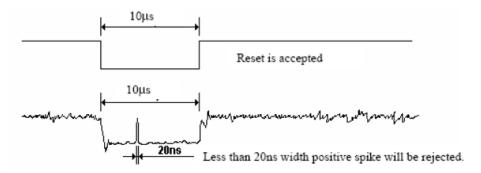
Notes:

- The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.
 - 2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

- 3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.
 - 4. Spike Rejection also applies during a valid reset pulse as shown below:





- 5. When Reset applied during Sleep In Mode.
- 6. When Reset applied during Sleep Out Mode.
- It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.



3. Optical Characteristics

LCD Module Specification

Item	Item		Condition	Min.	Typ.	Max.	Unit	Remark		
				75	80	1				
Viowing angle		θΒ	CR≥10	75	80	1	daaraa	Note5		
viewing a	Viewing angle		CR≥10	75	80	1	degree	Notes		
		θR		75	80	1				
Contrast 1	Ratio	CR	θ=0° optimal	700	900	-	-	Note3		
D	T:	T_R	T259C	-	12	16		N-4-2		
Response	Response Time		Ta=25°C	-	18	22	ms	Note2		
	White	X	θ=0°		-					
		у						-		
	Red	X		-0.05	0.631	+0.05		Note6		
Color		у			0.330					
Chromaticity	Green	X	0-0	-0.03	0.282	+0.03	-	Noteo		
	Green	у			0.544					
	Blue	X			0.138					
	Blue	y			0.155					
Uniform	nity	U	θ = 0 °	70	80	-	%	Note7		
Lumina	nce	L	$I_F=Typ.$	-	TBD	-	cd/m ²	Note8		

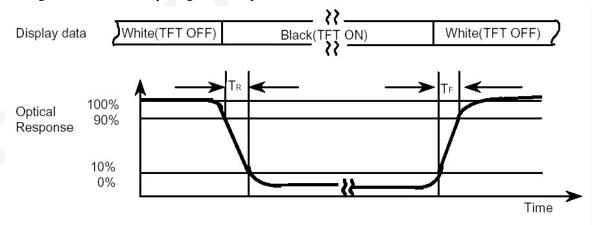
Note:

1. Test equipment setup

After stabilizing and leaving the panel alone at a given temperature for 30 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 with a viewing angle of 1 °at a distance of 50cm and normal direction.

2. Definition of response time: T_R and T_F

The figure below is the output signal of the photo detector.

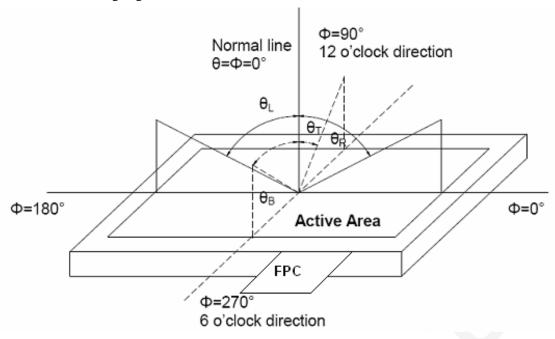


3. Definition of contrast ratio

4. The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.



5. Definition of viewing angle:



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6. Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

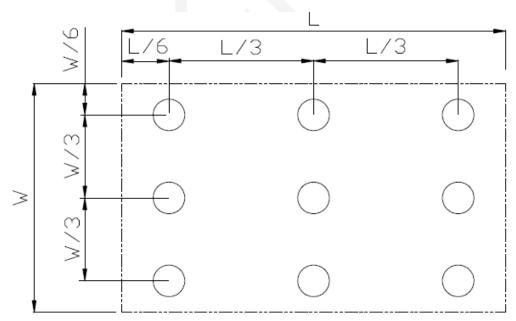
7. Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig.). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity(U) = L_{MIN} / L_{MAX}

L----Active area length

W---- Active area width



L_{MAX}: The measured maximum luminance of all measurement position.

 L_{MIN} : The measured minimum luminance of all measurement position.

8. Definition of Luminance:

Measure the luminance of white state at center point.



4. Reliability

4.1. Reliability Condition

No.	Item	Condition	Remark
1	High temperature	70°C, 240hrs	Finish product
	Operating	70 C, 240liis	(With polarizer)
2	Low temperature	-20°C, 240hrs	Finish product
	Operating	-20 C, 240ms	(With polarizer)
3	High temperature	90°C 240hra	Finish product
	Storage	80°C, 240hrs	(With polarizer)
4	Low temperature	200C 240hm	Finish product
	Storage	-30°C, 240hrs	(With polarizer)
5	High temperature &	90°C 000/ DII 240h	Finish product
3	Humidity Storage	80°C, 90%RH, 240hrs	(With polarizer)
6	Thermal Shock Storage	-30°C, 30min. <=> 80°C,30min.	Finish product
0	(No operation)	100 Cycles	(With polarizer)
	ESD Test	Voltage: +8KV	Finish product
7		R:330Ω, C:150pF	_
		Air discharge, 10 times	(With polarizer)
	Vibration Test	0.015G*G/Hz from 5-200HZ, -6dB/Octave	
8		from 200-500HZ	Finish product
8		2 hours for each direction of X. Y. Z.	(With polarizer)
		(6 hours for total)	
9	Drop Test	Packed, 60cm free fall	Finish product
9		1 corner, 3 edges, 6 surfaces	(With polarizer)

- Current consumption < 2 times of initial value
- Contrast > 1/2 initial value
- Function: work normally

^{*}One single product test for only one item.

^{*} Judgment after test: keep in room temperature for more than 2 hours.



4.2. Inspection plan

Class	Item	Judgment	Class		
	10	"Model no.", "lot no." and" quantity" should	3.6		
D 1: 0	1.Outside and inside package	indicate on the package.	Minor		
Packing &		Other model mixed rejected.	Critical		
Indicate	2.Model mixed and quantity	Quantity short or over rejected.			
	3.Product indication	"Model no." should indicate on the product	Major		
A accombly	4.Dimension,LCD glass scratch and	A coording to amodification on drawing	Major		
Assembly	scribe defect	According to specification or drawing			
	5. Viewing area	Polarizer edge or LCD's sealing line is visible in	Minor		
	3. Viewing area	the viewing area rejected			
	6.Blemish,black spot, white spot in	According to standard of visual inspection	Minor		
	the LCD and LCD glass cracks	(inside viewing area)	WIIIOI		
	7.Blemish,black spot White spot	According to standard of visual inspection	Minor		
	and scratch on the polarizer	(inside viewing area)	WITHOI		
	8.Bubble in polarizer	According to standard of visual inspection	Minor		
	o.Buoole in polarizer	(inside viewing area)	IVIIIIOI		
		Strong deviation color (or Newton ring) of LCD	Minor		
	9.LCD's rainbow color	rejected.			
	J.ECD STAINGOW COIG	Or according to limited sample (if needed, and			
Appearance		inside viewing area)			
		Burned area or wrong part number is on FPC.			
		The symbol, character, and mark of FPC are			
		unidentifiable.			
		The stripped solder mask, A>1.0mm.			
		0.3mm < stripped solder mask or visible circuit,			
	10.FPC	A<1.0mm,and the number is ≥ 4 pieces.	Minor		
	101210	Particle between circuits in solder mask.	1,111101		
		Circuit is peeled off or cracked.			
A		Any circuit risen or exposed.			
		$0.2 \text{mm} < \text{Area of solder ball, A is } \leq 0.4 \text{mm,the}$			
		number of solder ball is ≥ 3 pieces.			
	11.71	The magnitude of solder ball, A is > 0.4mm.			
	11.Electrical and optical	According to standard of visual inspection			
	characteristics (contrast, VOP,	(inside viewing area)	Critical		
	chromaticity etc.)	Missing day line 1	C :: 1		
	12.Missing pattern	Missing dot, line, character rejected	Critical		
E1- 4 1 1	13.Short circuit, wrong pattern	Non display, wrong pattern display, current	Critical		
Electrical	display	consumption out of specification rejected	M:		
	14.Pin hole, pattern deformity	According to standard of visual inspection	Minor		
	15.Black spot, white spot, black	Strong deviation color rejected	3.60		
	line, white line, slant line,	Or according to limited sample full off screen	Minor		
	background uneven, color uneven	(all black) disregards	3.41		
	16.Stick image (retention image)	Fixed test picture within two hours rejected	Minor		

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4.3. Standard of visual inspection

Class	Item	Judgment		
	Blemish, black spot, white spot in the LCD.	(A) Round type		Unit: mm
		Diameter (mm)		Acceptable Quantity
		0.25 < A		0
	Blemish, black spot, white spot and scratch on the polarizer.	Note: $A = (x + y)/2$ (mm)		
Minor		(B) Line type		Unit: mm
	• * • •	Length	Width	Acceptable Quantity
	$\begin{array}{c c} & & & \\ & & & &$	-	W ≦ 0.03	Acceptable
		L<5	$0.03 < W \le 0.07$	3
		L<5	$0.03 < W \le 0.07$	1
		-	0.07 <w< td=""><td>Follow round type</td></w<>	Follow round type
		Unit: mm		
		Diameter (mm)		Acceptable Quantity
		A < 0.3		Acceptable
Minor	Bubble in polarizer	0.3 < A < 0.5		1
		0.5 < A		0
		Unit: mm		
		Diameter (mm)		Acceptable Quantity
Minor	Pin hole, Pattern deformity	0.4 < A		0



5. Precautions

5.1. Handling Precautions

- (1) Protect the panel from static, it may cause damage to the CMOS Gate Array IC.
- (2) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

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- (3) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (4) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Don't use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (5) Pins of I/F connector shall not be touched directly with bare hands.
- (6) Refrain from strong mechanical shock and / or any force to the panel. In addition to damage, this may cause improper operation or damage to the panel.
- (7) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a B pencil lead.
- (8) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining and discoloration may occur.
- (9) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

5.2. Storage Precautions

- (1) Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the panel with temperature from 0 to 35°C and relative humidity of less than 70%.
- (2) The panel shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

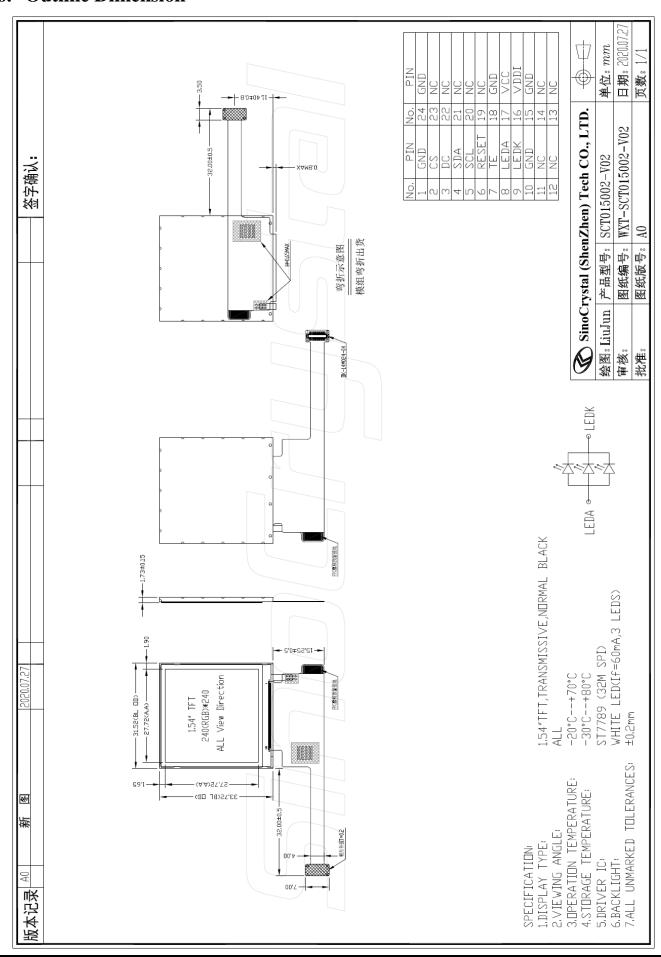
5.3. Operation Precautions

- (1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- (2) Do not exceed the absolute maximum rating value. (the supply voltage variation, Input voltage variation in part contents and environmental temperature and so on). Otherwise the panel may be damaged.
- (3) If the panel displays the same pattern continuously for a long period of time, it can be the situation when the image" Sticks" to the screen.

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6. Outline Dimension



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7. Packing Information

7.1. Packing Quantity

TBD.

7.2. Flowing chart

TBD.