


SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
MODEL	SC13264005-V30
CUSTOMER APPROVED	

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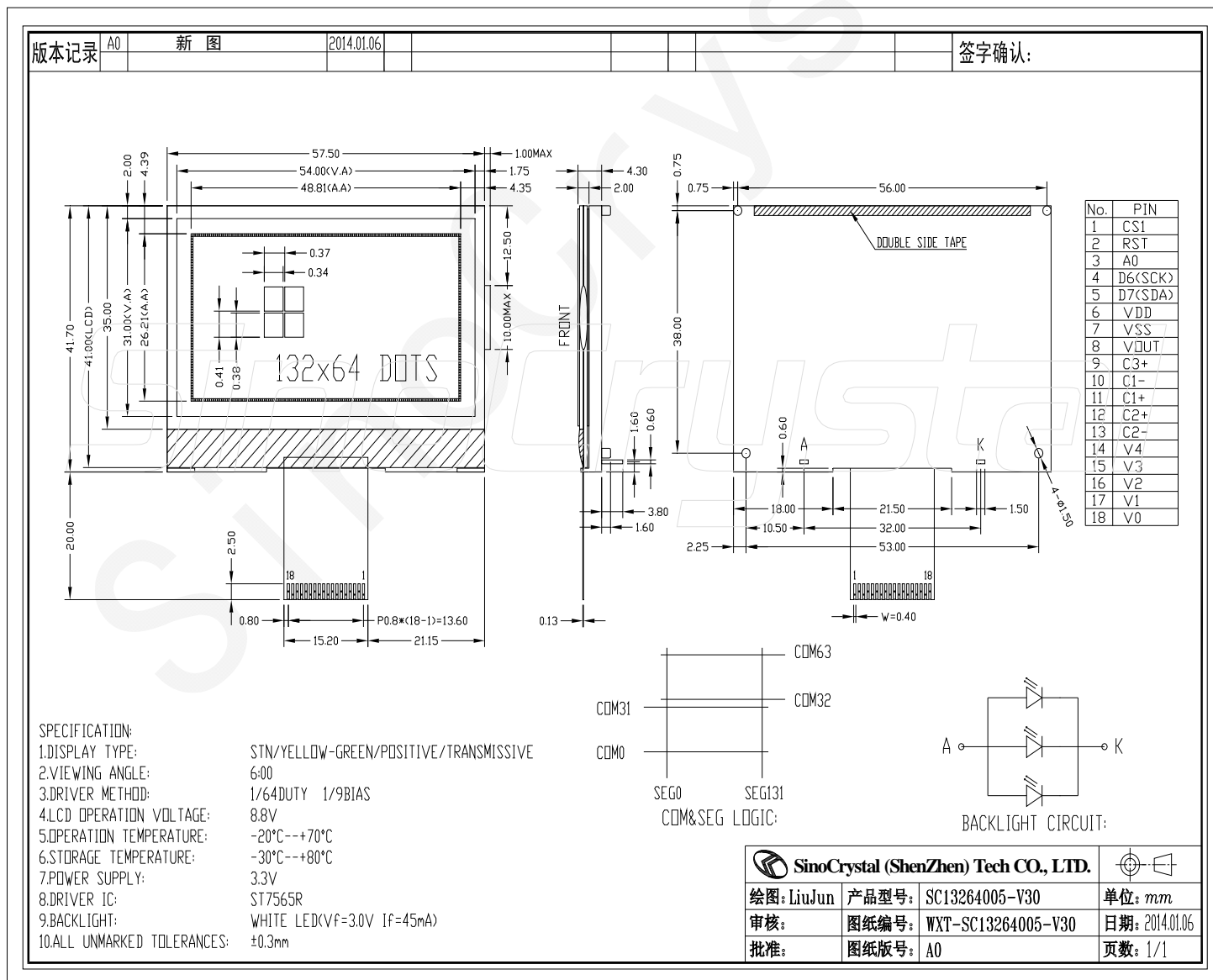
Specification Revision History

Version	Content	Date
A0	First Issue	04-May-2014

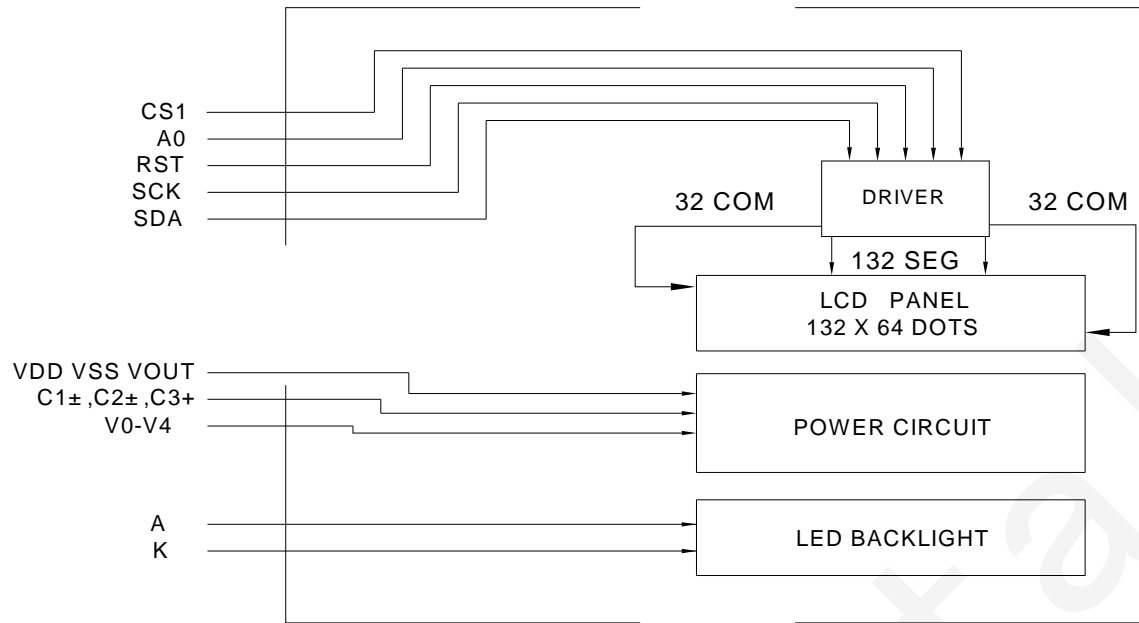
1. PHYSICAL DATA

ITEM	STANDARD VALUE	UNIT
NUMBER OF GRAPHIC	132×64	Mm
MODULE DIMENSION	57.5×41.7×8.1	Mm
VIEWING AREA	54.0×41.0	Mm
DOT SIZE	0.34×0.38	Mm
DOT PITCH	0.37×0.41	Mm
LCD TYPE	STN/YELLOW-GREEN/POSITIVE/TRANSMISSIVE	
DUTY	1/64	
VIEWING DIRECTION	6:00	o'clock
BACK LIGHT TYPE	SIDE LIT LED	
BACK LIGHT COLOR	WHITE	
APPROX. WEIGHT	TBD	G

2. EXTERNAL DIMENSIONS



3. BLOCK DIAGRAM



4. INTERFACE PIN CONNECTIONS

NO	SYMBOL	LEVEL	FUNCTION
1	CS1	H/L	Chip select signal
2	RST	H/L	Chip reset signal
3	A0	H/L	Register selection input H : Indicate that D0 to D7 are display data. L: Indicate that D0 to D7 are control data
4	D6(SCK)	H/L	D6 : the serial clock input (SCK)
5	D7(SDA)	H/L	D7 : serial data input (SDA) ;
6	VDD	3.3V	Supply voltage for logic
7	VSS	Ground	Ground
8	VOUT	--	Power for LCD
9	C3+	--	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal.
10	C1-	--	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1P terminal.
11	C1+	--	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP1N terminal. Reset signal.
12	C2+	--	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2N terminal.
13	C2-	--	DC/DC voltage converter. Connect a capacitor between this terminal and the CAP2P terminal. Chip select.
14-18	V4-V0	--	This is a multi-level power supply for the liquid crystal drive.

5. ABSOLUTE MAXIMUM RATINGS

(1) Electrical Absolute Ratings

Item	Symbol	Min.	Max.	Unit	Note
Power Supply for Logic	VDD		3.3	Volt	Note 1
Power Supply for LCD	V _{LCD}		12	Volt	
Input Voltage	V _I	0	V _{CC}	Volt	
Current for LED backlight	I _{LED}		60	mA	

Note 1: Operator should be grounded during handling LCM

(2) Environmental Absolute Maximum Ratings

Item	Normal Temperature				Wide Temperature			
	Operating		Storage		Operating		Storage	
	Min.	Max,	Min.	Max,	Min.	Max,	Min.	Max,
Ambient Temperature	0℃	+50℃	-10℃	+60℃	-20℃	+70℃	-30℃	+80℃
Humidity(without condensation)	Note 2,4		Note 3,5		Note 4,5		Note 4,6	

Note 2 Ta ≤ 50℃ : 80% RH max

Ta > 50℃ : Absolute humidity must be lower than the humidity of 85%RH at 50℃

Note 3 Ta at -20℃ will be <48hrs at 70℃ will be <120hrs when humidity is higher than 75%.

Note 4 Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 5 Ta ≤ 70℃ : 75RH max

Ta > 70℃ : absolute humidity must be lower than the humidity of 75%RH at 70℃

Note 6 Ta at -20℃ will be <48hrs, at 80℃ will be <120hrs when humidity is higher than 75%.

6. ELECTRICAL CHARACTERISTICS

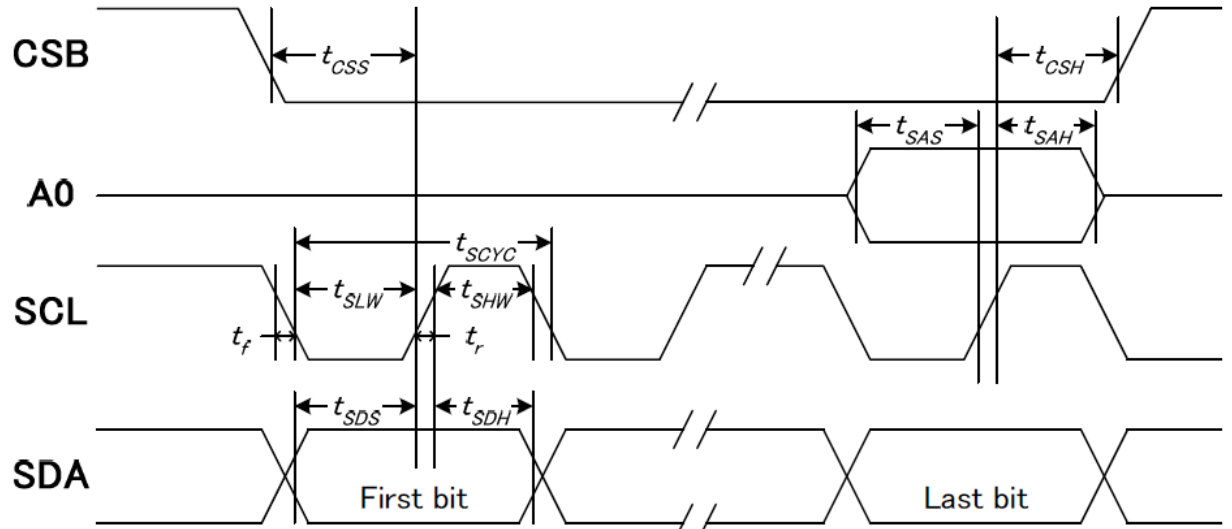
DC Characteristics

(VSS=0V, VDD=3.3V, Ta=25°C)

Item	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Power Supply for Logic	VDD		-	-	3.3	Volt
Input Voltage	V _{IH}		0.7VDD	-	VDD	Volt
	V _{IL}		0	-	0.3VDD	Volt
Output Voltage	V _{OH}	I _{OUT} =1mA	0.8VDD	-	VDD	Volt
	V _{OL}	I _{OUT} =-1mA	VSS	-	0.2VDD	Volt
LCM Recommend LCD Module Driving Voltage	V _{LCD}	T _a =0°C	-	-	-	Volt
		T _a =25°C	9.3	10.5	11.5	
		T _a =50°C	-	-	-	
Power Supply Current for LCM	I _{DD} (BL OFF)	-	-	-	2	mA
	I _{DD} (BL ON)	-	-	-	47	
Power Supply for LED Backlight	V _{BLA} - V _{BLK}	Ta=25°C	-	-	3.3	V

AC Characteristics

System Bus Timing for 4-Line Serial Interface



(VDD = 3.3V, Ta = 25 °C)

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period	SCLK	tSCYC		50	—	ns
SCLK "H" pulse width		tSHW		25	—	
SCLK "L" pulse width		tSLW		25	—	
Address setup time	A0	tSAS		20	—	
Address hold time		tSAH		10	—	
Data setup time	SDA	tSDS		20	—	
Data hold time		tSDH		10	—	
CSB-SCLK time	CSB	tCSS		20	—	
CSB-SCLK time		tCSH		40	—	

(VDD = 2.8V, Ta = 25 °C)

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period	SCLK	tSCYC		100	—	ns
SCLK "H" pulse width		tSHW		50	—	
SCLK "L" pulse width		tSLW		50	—	
Address setup time	A0	tSAS		30	—	
Address hold time		tSAH		20	—	
Data setup time	SDA	tSDS		30	—	
Data hold time		tSDH		20	—	
CSB-SCLK time	CSB	tCSS		30	—	
CSB-SCLK time		tCSH		60	—	

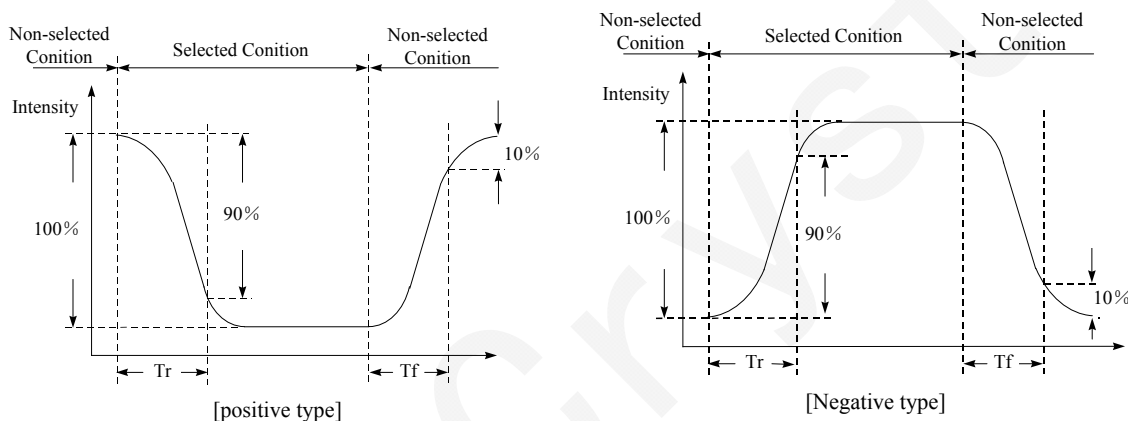
*1 The input signal rise and fall time (tr, tf) are specified at 15 ns or less.

*2 All timing is specified using 20% and 80% of VDD as the standard.

7. ELECTRO-OPTICAL CHARACTERISTICS

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	note
Viewing angle range	θ_f (12 o'clock)	When $Cr \geq 2$	---	15	---	Degree	Note 2 Note 3 Note 4
	θ_b (6 o'clock)		---	45	---		
	θ_l (9 o'clock)		---	30	---		
	θ_r (3 o'clock)		---	30	---		
Rise Time	T_r	$V_0-V_{SS}=10.5V$ $T_a=25^\circ C$		112		mS	Note 1
Fall Time	T_f			250			
Contrast	Cr		---	5.4	---		

[Note 1] Definition of Response Time (T_r , T_f)

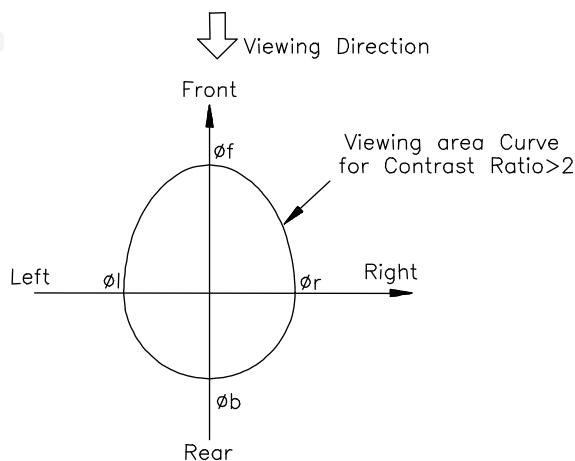


Conditions:

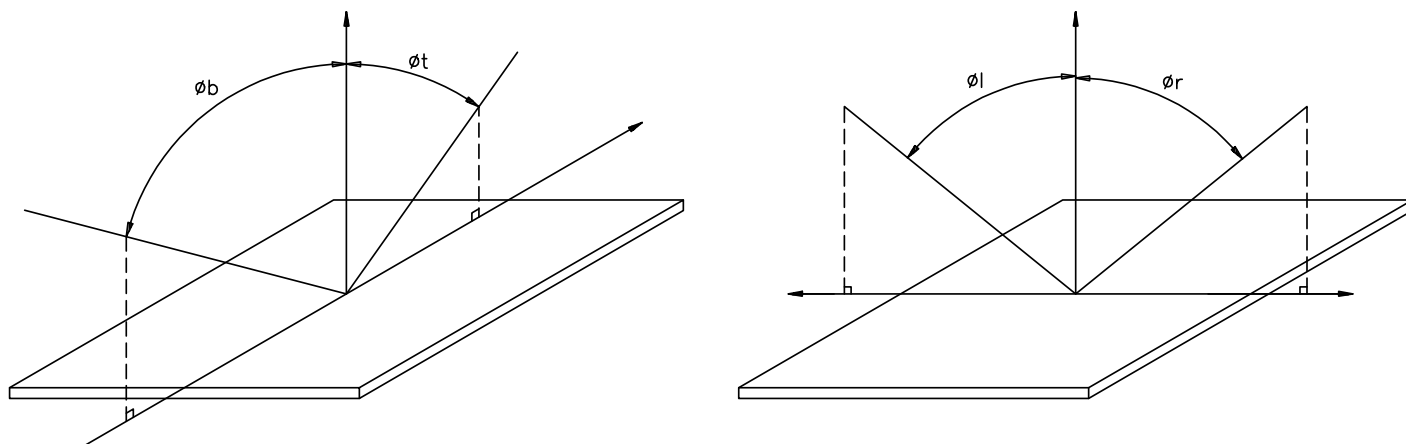
Operating Voltage : V_{op}
Frame Frequency : 64 Hz

Viewing Angle(θ , φ): 0° , 0°
Driving Wave form : 1/N duty, 1/a bias

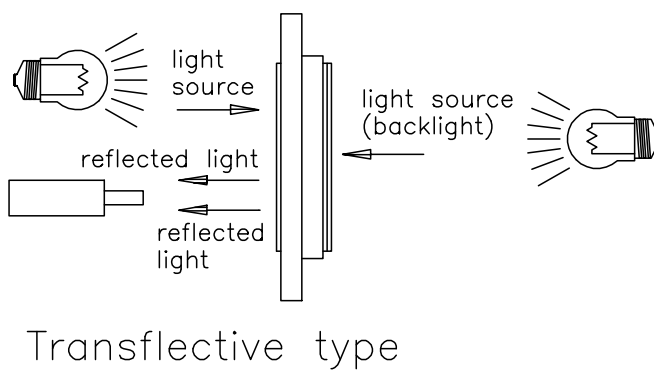
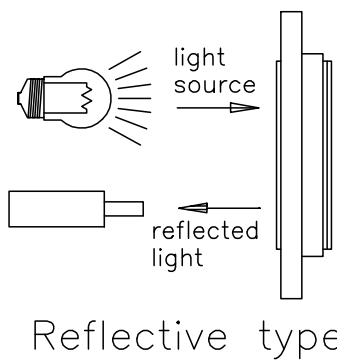
[Note 2] Definition of Viewing Direction



[Note 3] Definition of viewing angle



[Note 4] Description of Measuring Equipment



8. OPERATING PRINCIPLES & METHODS

Table of ST7565R Commands

(Note) *: ignored data

Command	Command Code										Function	
	A0	/RD	/WR	D7	D6	D5	D4	D3	D2	D1		D0
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0 1	LCD display ON/OFF 0: OFF, 1: ON
(2) Display start line set	0	1	0	0	1	Display start address					Sets the display RAM display start line address	
(3) Page address set	0	1	0	1	0	1	1	Page address			Sets the display RAM page address	
(4) Column address set upper bit Column address set lower bit	0	1	0	0	0	0	1 0	Most significant column address Least significant column address			Sets the most significant 4 bits of the display RAM column address. Sets the least significant 4 bits of the display RAM column address.	
(5) Status read	0	0	1	Status			0	0	0	0	0	Reads the status data
(6) Display data write	1	1	0	Write data							Writes to the display RAM	
(7) Display data read	1	0	1	Read data							Reads from the display RAM	
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0 1	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9) Display normal/reverse	0	1	0	1	0	1	0	0	1	1	0 1	Sets the LCD display normal/ reverse 0: normal, 1: reverse
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0 1	Display all points 0: normal display 1: all points ON
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0 1	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565R)
(12) Read-modify-write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
(14) Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset
(15) Common output mode select	0	1	0	1	1	0	0	0	*	*	*	Select COM output scan direction 0: normal direction 1: reverse direction
(16) Power control set	0	1	0	0	0	1	0	1	Operating mode			Select internal power supply operating mode
(17) V ₀ voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Resistor ratio			Select internal resistor ratio(Rb/Ra) mode
(18) Electronic volume mode set Electronic volume register set	0	1	0	1	0	0	0	0	0	0	1	Set the V ₀ output voltage electronic volume register
(19) Sleep mode set	0	1	0	1	0	1	0	1	1	0	0 1	0: Sleep mode, 1: Normal mode
(20) Booster ratio set	0	1	0	1	1	1	1	1	0	0	0	select booster ratio 00: 2x,3x,4x 01: 5x 11: 6x
(21) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(22) Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command

NOTE: For more detail information, please refer to the ST7565R's specification.

9. Display Data RAM (DDRAM)

ST7565R is built-in a RAM with 65X132 bit capacity which stores the display data. The display data RAM (DDRAM) store the dot data of the LCD. It is an addressable array with 132 columns by 65 rows (8-page with 8-bit and 1-page with 1-bit). The X-address is directly related to the column output number. Each pixel can be selected when the page and column addresses are specified (please refer to Fig 7 for detailed illustration). The rows are divided into: 8 pages (Page-0 ~ Page-7) each with 8 lines (for COM0~63) and Page-8 with only 1 line (COMS, for icon). The display data (D7~D0) corresponds to the LCD common-line direction and D0 is on top. All pages can be accessed through D[7:0] directly except icon page. Icon RAM uses only 1-bit of data bus (D0). Refer to Fig 8 for detailed illustration. The microprocessor can write to and read from (only Parallel interfaces) DDRAM by the I/O buffer. Since the LCD controller operates independently, data can be written into DDRAM at the same time as data is being displayed without causing the LCD flicker or data-conflict.

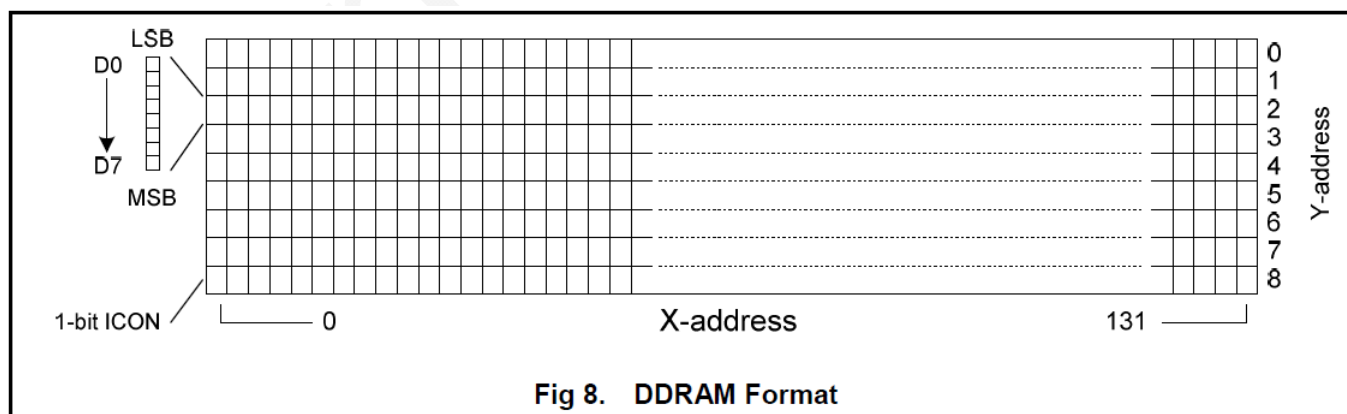
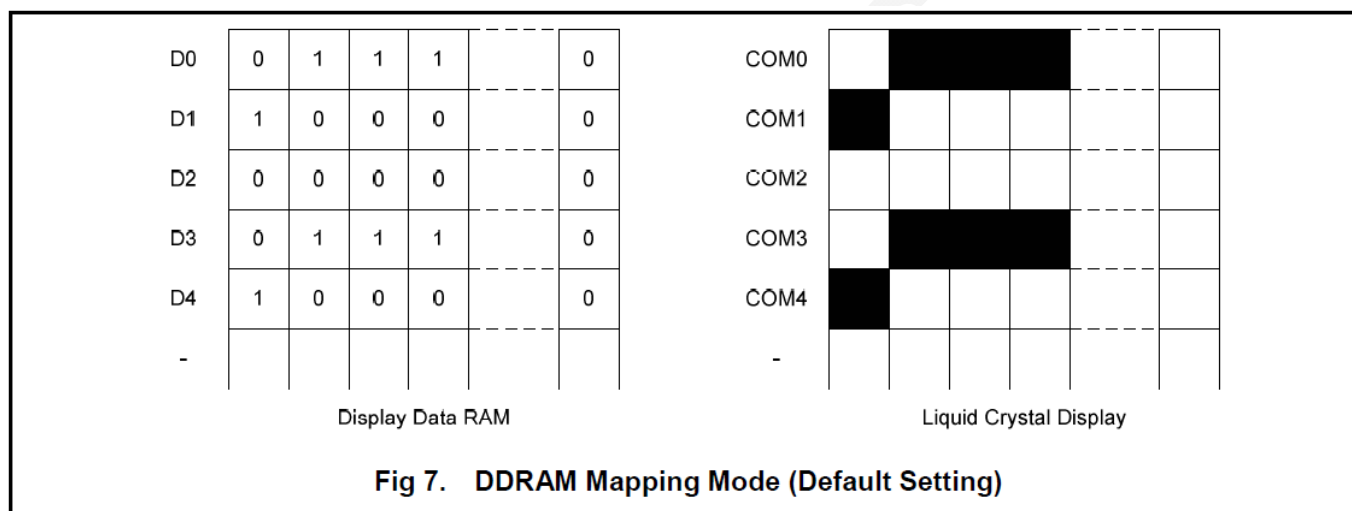


Fig 9. DDRAM and Output Map (COM/SEG)

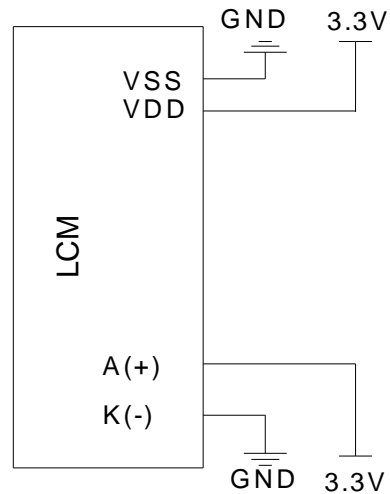
10.RELIABILITY

Environmental Test				
No.	Test Item	Content of Test	Test Condition	Applicable Standard
1	High temperature storage	Endurance test applying the high storage temperature for a long time.	80 °C 200 hrs	-----
2	Low temperature storage	Endurance test applying the low storage temperature for a long time.	-30 °C 200 hrs	-----
3	High temperature operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70 °C 200 hrs	-----
4	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time.	-20 °C 200 hrs	-----
5	High temperature / Humidity storage	Endurance test applying the high temperature and high humidity storage for a long time.	70 °C , 90 %RH 96 hrs	MIL-202E-103B JIS-C5023
6	High temperature / Humidity operation	Endurance test applying the electric stress (Voltage & Current) and temperature / humidity stress to the element for a long time.	50 °C , 90 %RH 96 hrs	MIL-202E-103B JIS-C5023
7	Temperature cycle	Endurance test applying the low and high temperature cycle. <div><div><div>-10°C</div><div>⇐</div><div>25°C</div><div>⇐</div><div>60°C</div></div><div><div>⇐30min</div><div>5min</div><div>⇐30min</div></div><div>1 cycle</div></div>	-10°C / 60°C 10 cycles	-----
Mechanical Test				
8	Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz → 1.5mmp-p 22~500Hz → 1.5G Total 0.5hrs	MIL-202E-201A JIS-C5025 JIS-C7022-A-10
9	Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G half sign wave 11 msdc 3 times of each direction	MIL-202E-213B
10	Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115 mbar 40 hrs	MIL-202E-105C
Others				
11	Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V , RS=1.5 kΩ CS=100 pF 10 time	MIL-883B-3015.1
Inspection after test: Inspection after 2~4 hours storage at room temperature ,the sample shall be free from defects: 1. Air bubble in the LCD. 2. Sealleak 3. Non-display. 4. Missing segments. 5. Glass crack. 6. Current Idd is twice higher than initial value.				

11.QUALITY GUARANTEE

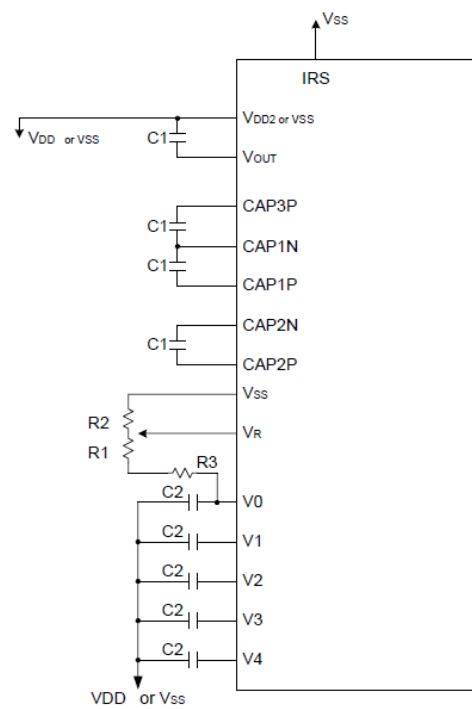
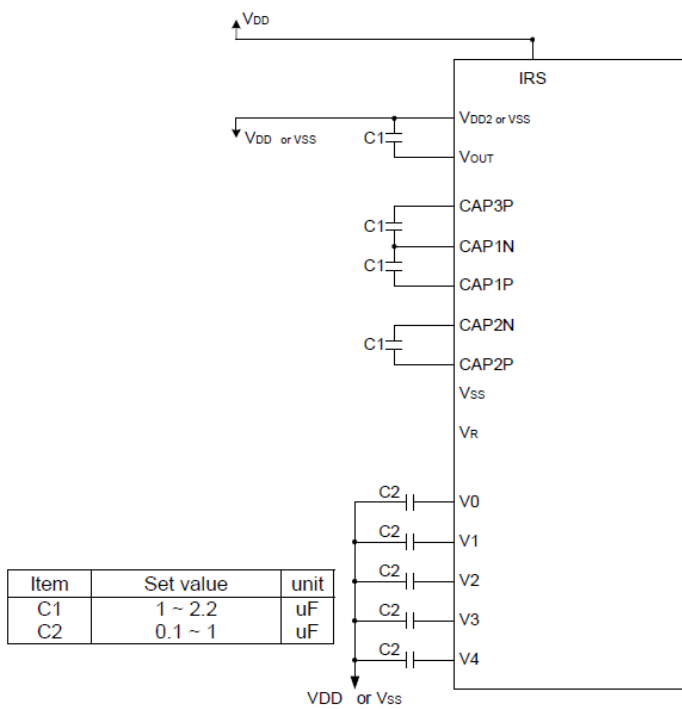
No	Item	Criteria		
1	inclusions (black spot, white spot, dust)	(1)round type diameter mm(a*) no of defect* $a \leq 0.20$ neglect $0.20 < a \leq 0.35$ 5max $0.35 < a$ none (2)linear type length mm(l) width mm(W) no. of defect na $W \leq 0.03$ neglect $1 \leq 3$ $0.03 < W \leq 0.08$ 6 $3 < l$ $0.08 < W$ none		
2	scratch	1.scratch on protective film is permitted. 2.scratch on polarizer shall be as follow: (1)round type diameter mm(a*) no of defect $a \leq 0.15$ neglect $0.15 < a \leq 0.20$ 2 max $0.20 < a$ none (2)linear type be judged by 1.-(2) linear type		
3	dent	diameter < 1.5mm		
4	bubble	not exceeding 0.5mm average diameter is acceptable between glass and polarizing film		
5	pin hole	$(a+b)/2 \leq 0.15\text{mm}$ maximum number: ignored $0.15 < (a+b)/2 \leq 0.20\text{mm}$ maximum number:10		
6	dot width	design width $\pm 15\%$		
7	dot defect	$(a+b)/2 \leq 0.20\text{mm}$ maximum number: ignored $0.20 < (a+b)/2 \leq 0.30\text{mm}$ maximum number:5 x=width		
8	contrast irregularity(spot)	diameter spec no of defect $a \leq 0.50\text{mm}$ neglect $0.50 < a \leq 0.75$ 5 $0.75 < a \leq 1.00$ 3 $1.00 < a$ none		
9	color tone and uniformity	obvious uneven color is not permitted		

12. POWER SUPPLY FOR LCM MODULE



(1) When the voltage regulator internal resistor is used.

(2) When the voltage regulator internal resistor is not used.



13. USING LCD MODULES

13-1. Liquid Crystal Display Modules

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

(1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

(2) Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).

(3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.

(4) When the display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum benzin. Do not scrub hard to avoid damaging the display surface.

(5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.

(6) Avoid contacting oil and fats.

(7) Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.

(8) Do not put or attach anything on the display area to avoid leaving marks on.

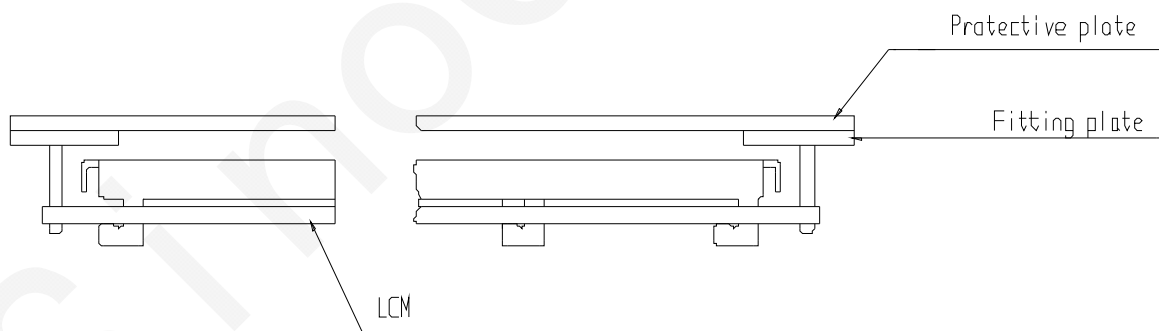
(9) Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).

(10) As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

13-2. Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

(1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



(2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be $\pm 0.1\text{mm}$.

13-3. Precaution for Handling LCD Modules

Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

(1) Do not alter, modify or change the shape of the tab on the metal frame.

(2) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.

(3) Do not damage or modify the pattern writing on the printed circuit board.

(4) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.

(5) Except for soldering the interface, do not make any alterations or modifications with a soldering iron.

(6) Do not drop, bend or twist LCM.

LCM is easy to be damaged. Please note below and be careful for handling.

Correct handling:

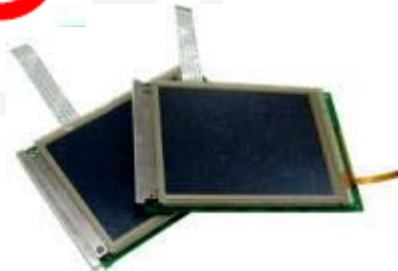


As above picture, please handle with anti-static gloves around LCM edges.

Incorrect handling:



Please don't touch IC directly.



Please don't stack LCM.



Please don't hold the surface of panel.



Please don't stretch interface of output, such as FPC cable.



Please don't hold the surface of IC.



Please don't operate with sharp stick such as pens.

13-4. Electro-Static Discharge Control

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

- (1) Make certain that you are grounded when handling LCM.
- (2) Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- (6) To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

13-5. Precaution for soldering to the LCM

- (1) Observe the following when soldering lead wire, connector cable and etc. to the LCM.
 - Soldering iron temperature : $280^{\circ}\text{C} \pm 10^{\circ}\text{C}$.
 - Soldering time : 3-4 sec.
 - Solder : eutectic solder.

If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.

(2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.

(3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

13-6. Precautions for Operation

(1) Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.

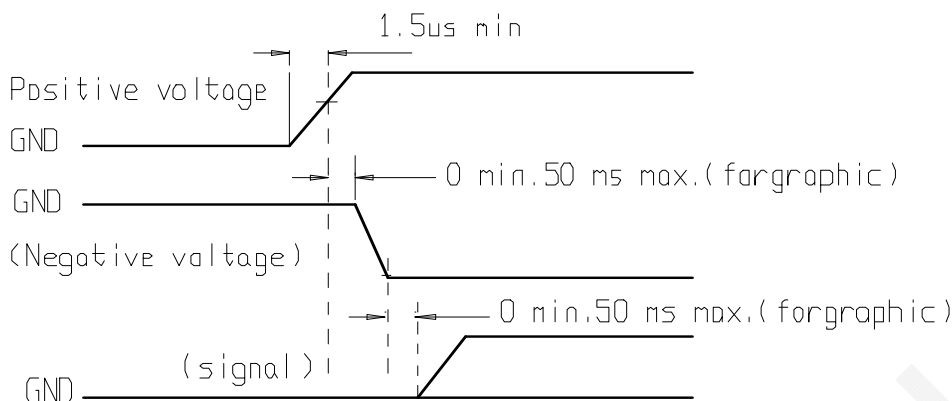
(2) Driving the LCD in the voltage above the limit shortens its life.

(3) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.

(4) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.

(5) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40°C , 50% RH.

(6) When turning the power on, input each signal after the positive/negative voltage becomes stable.



13-7. Storage

When storing LCDs as spares for some years, the following precaution are necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped.)
- (4) Environmental conditions :
 - Do not leave them for more than 168hrs. at 60°C.
 - Should not be left for more than 48hrs. at -20°C.

13-8. Safety

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

13-9.Return LCM under warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

- Broken LCD glass.
- PCB eyelet's damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.