


SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
MODEL	SCT035005-V01
CUSTOMER APPROVED	

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0158

RECORDS OF REVISIONS

Revision No	Revision Date	Description
Ver: A0	2015-03-03	First release

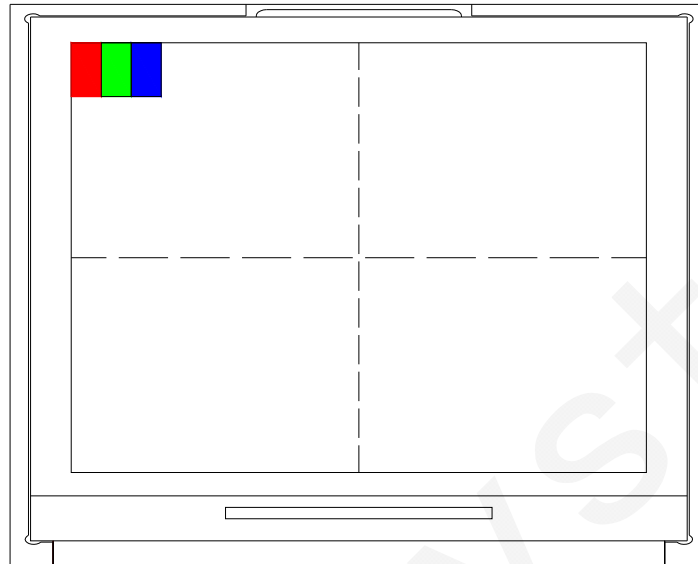
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- General Description
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1. General Description

This LCM [SCT035005-V01](#) is a TFT LCD module, comprising a 960-channel source driver, a 240-channel gate driver, 320 (RGB) x 240 dots graphic, and power supply circuit. The 262k color can be display.

This TFT-LCD has 3.5 inch diagonally measured active display area with QVGA resolution.



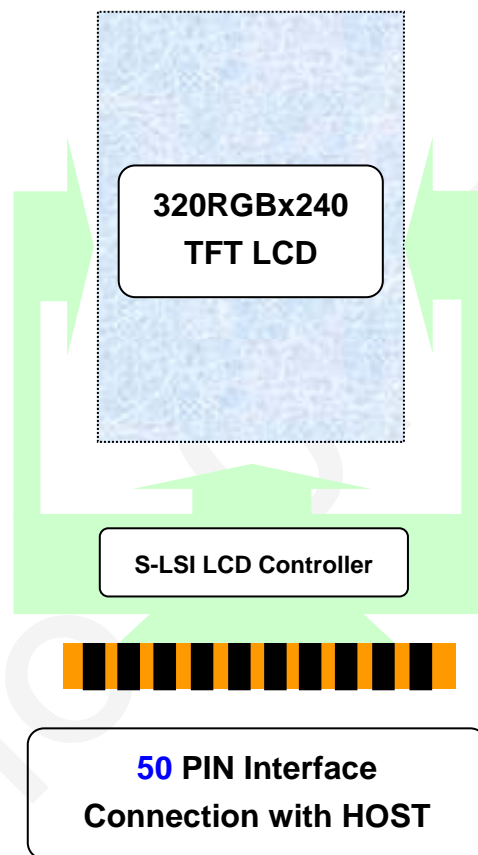
1.1 Mechanical Specifications

Item	Nominal Dimension	Unit
Dot Matrix	320 x RGB x 240	Dots
Module Size (W×H×T)	76.90 x 63.90 x 3.05	mm.
Active Area (W×H)	70.08 x 52.56	mm.
Pixel arrangement	RGB Stripe	mm.
Dot Pitch (W×H)	0.219 x 0.219	mm.
Color depth	262K (MAX)	colors
Interface	MCU & RGB	-
Driving IC Package	COG	-

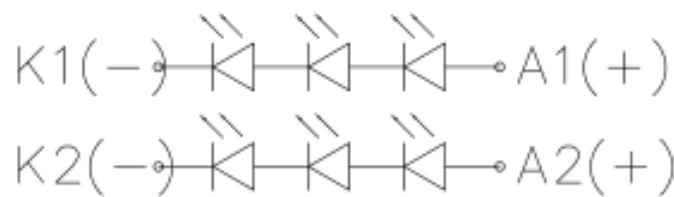
1.2 Display Specifications

Item	Nominal Dimension	Unit
Operating temperature	-20 ~70	°C
Storage temperature	-30~80	°C
LCD Type	a-Si TFT	-
LCD Mode	TN/Normal White	-
Backlight Type	LED x 6	PCS

1.3 Block Diagram



1.4 Back-light Unit



1.5 Interface Pin

Pin No	Pin Symbol	Level	Description																																																																											
1	GND	0V	Ground																																																																											
2-3	NC	-	No use																																																																											
4-6	GND	0V	Ground																																																																											
7	RD	H/L	6800-system : E (enable signal) 8080-system : RD (read strobe signal)																																																																											
8	SDO	H/L	Data output pin in serial interface																																																																											
9	RESET	H/L	Reset signal																																																																											
10	CS	H/L	Chip Select																																																																											
11	SCL	H/L	Serial clock input																																																																											
12	SDA	H/L	Data input pin in serial interface																																																																											
13	RS	H/L	selector of command or parameter																																																																											
14	WR	H/L	6800-system : RW (indicates read cycle when High, write cycle when Low) 8080-system : WR (write strobe signal)																																																																											
15	PS3	H/L	<table border="1"> <thead> <tr> <th>PS3</th> <th>PS2</th> <th>PS1</th> <th>PS0</th> <th>Interface Mode</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>16-bit 6800 parallel interface</td> </tr> <tr> <td>0</td> <td>0</td> <td>0</td> <td>1</td> <td>8-bit 6800 parallel interface</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>0</td> <td>16-bit 8080 parallel interface</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>1</td> <td>8-bit 8080 parallel interface</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>0</td> <td>9-bit generic D[17:9] (262k colour) + 3-wire SPI If 65K color, D12 shorts to D17 internally</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>1</td> <td>16-bit generic (262k colour) + 3-wire SPI</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>0</td> <td>18-bit generic (262k colour) + 3-wire SPI</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>1</td> <td>6-bit generic D[17:12] (262k colour) + 3-wire SPI</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> <td>0</td> <td>18-bits 6800 parallel interface</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> <td>1</td> <td>9-bits 6800 parallel interface</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>0</td> <td>18-bit 8080 parallel interface</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>1</td> <td>9-bit 8080 parallel interface</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> <td>0</td> <td>3-wire SPI</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> <td>1</td> <td>4-wire SPI</td> </tr> </tbody> </table>	PS3	PS2	PS1	PS0	Interface Mode	0	0	0	0	16-bit 6800 parallel interface	0	0	0	1	8-bit 6800 parallel interface	0	0	1	0	16-bit 8080 parallel interface	0	0	1	1	8-bit 8080 parallel interface	0	1	0	0	9-bit generic D[17:9] (262k colour) + 3-wire SPI If 65K color, D12 shorts to D17 internally	0	1	0	1	16-bit generic (262k colour) + 3-wire SPI	0	1	1	0	18-bit generic (262k colour) + 3-wire SPI	0	1	1	1	6-bit generic D[17:12] (262k colour) + 3-wire SPI	1	0	0	0	18-bits 6800 parallel interface	1	0	0	1	9-bits 6800 parallel interface	1	0	1	0	18-bit 8080 parallel interface	1	0	1	1	9-bit 8080 parallel interface	1	1	1	0	3-wire SPI	1	1	1	1	4-wire SPI
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18	PS0	H/L																																																																												
19-36	DB17 – DB0	H/L	Data bus , Fix to VSS level when not in use When 16-bit be selected, D[17:10] and D[8:1] used																																																																											
37	DEN	H/L	Display enable pin from controller.																																																																											
38	VSYNC	H/L	Frame/Ram Write Synchronization input																																																																											
39	HSYNC	H/L	Line Synchronization input																																																																											
40	DCLK	H/L	Dot-clock signal and oscillator source. A non-stop external clock must be provided to that pin even at front or black porch non-display period.																																																																											
41	NC	-	No use																																																																											

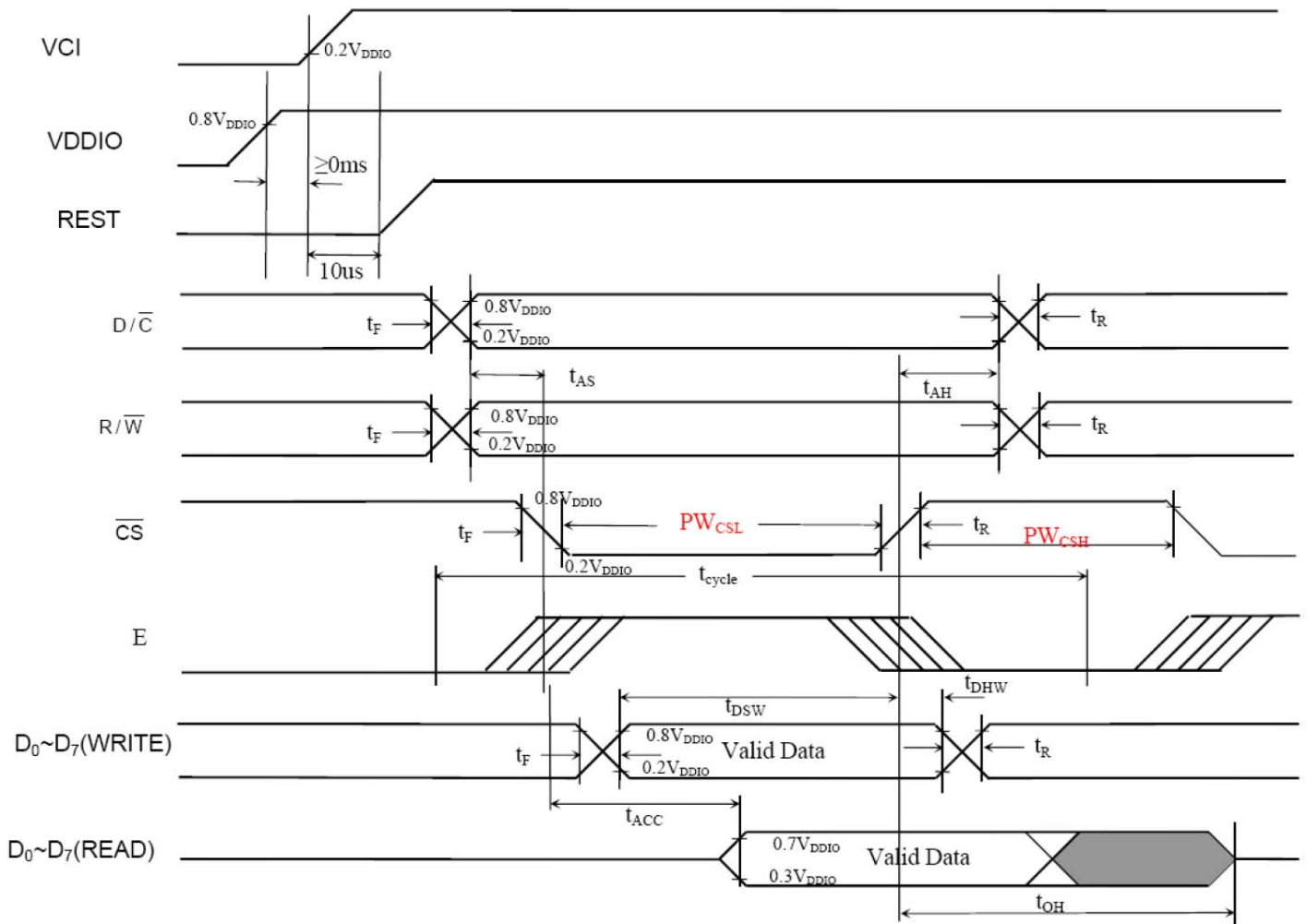
42-43	GND	0V	Ground
44-45	VDD	2.8-3.3V	Power supply
46	NC	-	No use
47	K2	-	LED light, cathode
48	A2		LED light, anode
49	A1		LED light, anode
50	K1	-	LED light, cathode

2. Interface Timing

Parallel 6800 Timing Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
t_{cycle}	Clock Cycle Time (write cycle)	75	-	-	ns
t_{cycle}	Clock Cycle Time (read cycle) (Based on $V_{OL}/V_{OH} = 0.3 \cdot V_{DDIO}/0.7 \cdot V_{DDIO}$)	450	-	-	ns
t_{AS}	Address Setup Time (R/W)	0	-	-	ns
t_{AH}	Address Hold Time (R/W)	0	-	-	ns
t_{DSW}	Data Setup Time (D0~D7, WRITE)	5	-	-	ns
t_{DHW}	Data Hold Time (D0~D7, WRITE))	5	-	-	ns
t_{ACC}	Data Access Time (D0~D7, READ)	200	-	-	ns
t_{OH}	Output Hold time (D0~D7, READ)	100	-	-	ns
PW_{CSL}	Pulse width /CS low (write cycle)	40	-	-	ns
PW_{CSH}	Pulse width /CS high (write cycle)	25	-	-	ns
PW_{CSL}	Pulse width /CS low (read cycle)	225	-	-	ns
PW_{CSH}	Pulse width /CS high (read cycle)	225	-	-	ns
t_R	Rise time	-	-	15	ns
t_F	Fall time	-	-	15	ns

Note: CS can be pulled low during the write cycle, only /WR is needed to be toggled

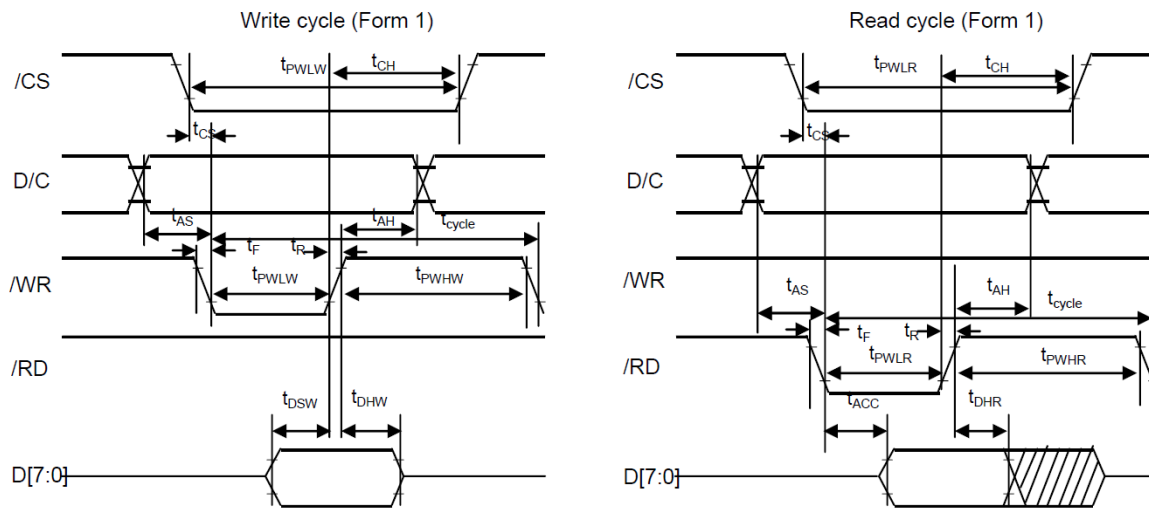


Parallel 8080 Timing Characteristics

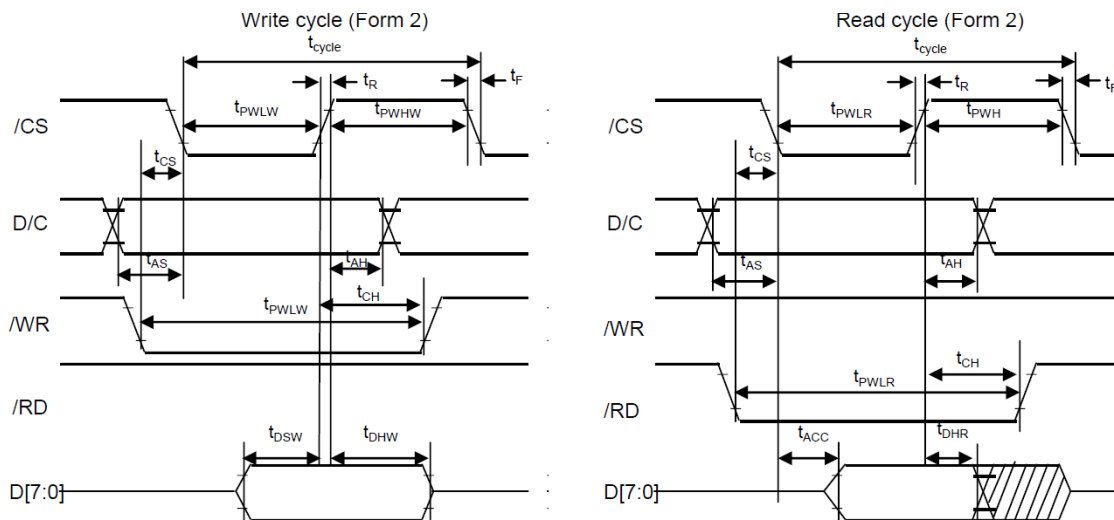
Symbol	Parameter	Min	Typ	Max	Unit
t_{cycle}	Clock Cycle Time (write cycle)	100	-	-	ns
t_{AS}	Address Setup Time	10	-	-	ns
t_{AH}	Address Hold Time	0	-	-	ns
t_{CS}	Chip Select Time	0	-	-	ns
t_{CH}	Chip Select Hold Time	0	-	-	ns
t_{DSW}	Write Data Setup Time	10	-	-	ns
t_{DHW}	Write Data Hold Time	10	-	-	ns
t_{DHR}	Read Data Hold Time	100	-	-	ns
t_{ACC}	Access Time (RAM)	250	-	-	ns
	Access Time (command)	250	-	-	ns
t_{PWLR}	Chip Select Low Pulse Width (read RAM)	500	-	-	ns
t_{PWLR}	Chip Select Low Pulse Width (read Command)	500	-	-	ns
t_{PWLW}	Chip Select Low Pulse Width (write)	50	-	-	ns
t_{PWHR}	Chip Select High Pulse Width (read)	500	-	-	ns
t_{PWHW}	Chip Select High Pulse Width (write)	50	-	-	ns
t_R	Rise Time	-	-	15	ns
t_F	Fall Time	-	-	15	ns

Note: All timings are based on 20% to 80% of $V_{DDIO}-V_{SS}$

8080-series parallel interface characteristics (Form 1: /CS low pulse width > W/ R low pulse width)



8080-series parallel interface characteristics (Form 2: /CS low pulse width < W/ R low pulse width)



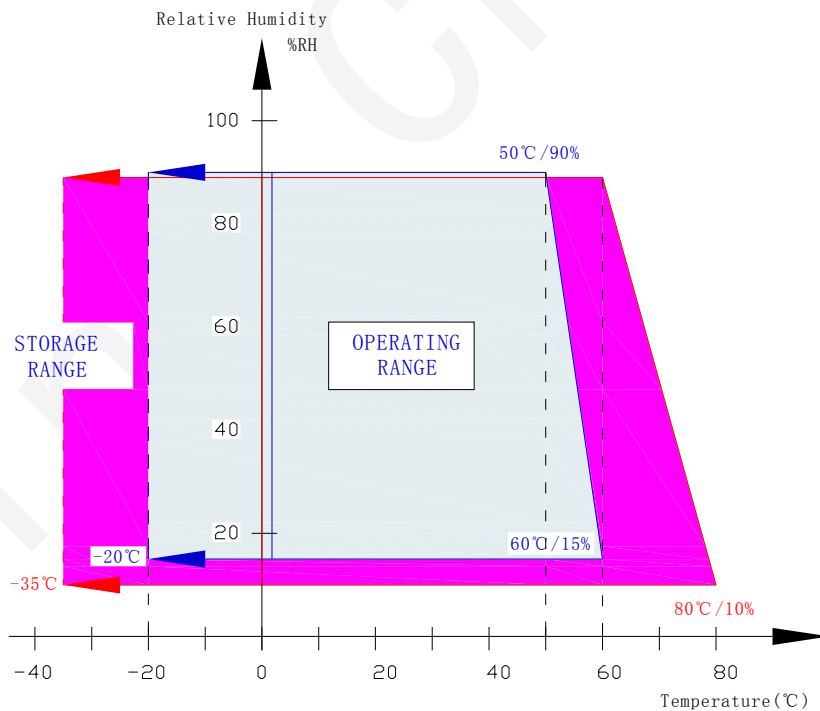
3. Electrical Characteristics

3.1 Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit
Supply voltage for System	VDD	0	+3.6	V
Supply voltage for Interface Operation	VDD	0	+3.6	V
Operate temperature range	TOP	-20	70	°C
Storage temperature range	TST	-30	80	°C

Note:

- (1) 90%RH maximum humidity, 60°C maximum wet-bulb temperature When operated at a temperature lower than 0°C, the LCD worked slowly and the screen appeared low-contrast images due to the characteristics of LC(Liquid Crystal).
- (2) If any fixed pattern is displayed on LCD for minutes, image-sticking phenomenon may occur.
- (3) Degradation could occur to pixels' TFT when DC BIOS is input into its gate-signal under POWER OFF WAITING STAND-BY & SLEEP MODE. Therefore, LCD should be turn off then.
- (4) Please operate a LCD module on the basis of the recommended S/W(Register)



Temperature & Humidity Graph at Absolute Environment

DATA). If you want to change any part of the S/W, you must take driver's confirmation.

3.2 DC Characteristics

T_a = 25°C

Item	Symbol	Min	Typ	Max	Unit	Condition
Supply voltage for System	VDD	2.5	2.8	3.3	V	
Supply voltage for Interface Operation	VDD	2.5	2.8	3.3	V	
Input high level voltage	V _{IH}	0.8VDD	--	VDD	V	
Input low level voltage	V _{IL}	0	--	0.2VDD	V	
Power supply current	I _{CC} + I _{CI}	--	TBD	--	mA	
Backlight forward voltage	V _F	8.4	9.6	10.6	V	
Backlight forward current	I _F	--	--	20x2	mA	
Backlight Uniformity	Avg	80	85	--	%	

4. Optical characteristics

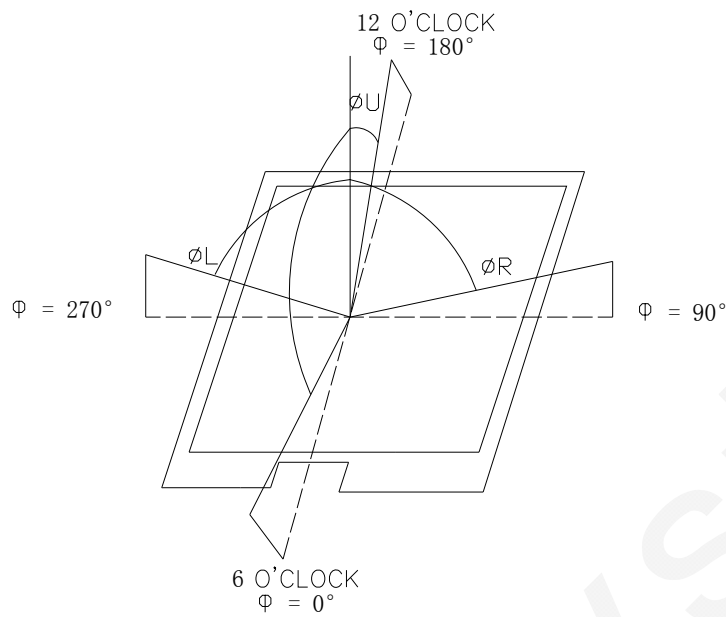
Parameter		Symbol	Condition	Min	Typ	Max	Unit	Note
Viewing angle		Left	CR _≥ 10		45		Degree	(2)
		Right			45		Degree	
		Up			25		Degree	
		Down			45		Degree	
Color Chromaticity	Red	R _x	θ=0 Normal viewing angle	0.604	0.624	0.644	-	Color Chromaticity
		R _y		0.302	0.322	0.342	-	
	Green	G _x		0.268	0.288	0.308	-	
		G _y		0.54	0.56	0.58	-	
	Blue	B _x		0.127	0.147	0.167	-	
		B _y		0.097	0.117	0.137	-	
	White	W _x		0.247	0.307	0.367	-	
		W _y		0.268	0.328	0.388	-	
Contrast ratio		CR	optimal	-	250		-	(1)
Response time		Tr+Tf			35	50	ms	(3)
Luminance on surface If=40mA		L _v	Normally θ _x = θ _y = 0	250	300		cd/m ²	

Note (1) Definition of contrast ratio

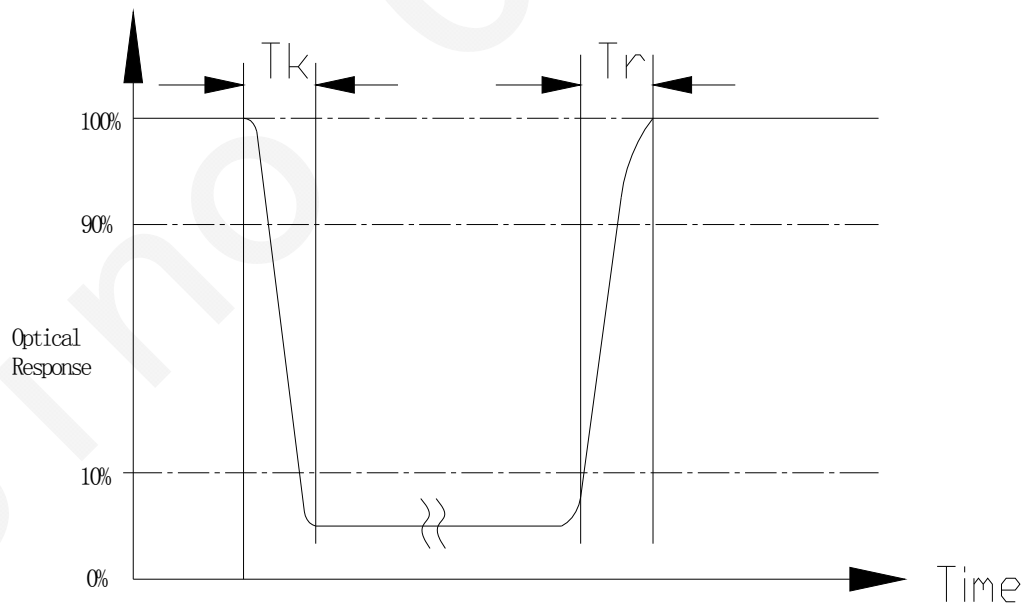
Measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixel white}}{\text{Luminance with all pixel black}}$$

Note (2) Definition of viewing angle



Note (3) Definition of response time: Tr+Tf



5. Reliability

5.1 Reliability Condition

*One single product test for only one item.

Item No	Item	Condition	Remark
1	High temperature Operating	60°C, 120Hours	Finish product (With polarizer)
2	Low temperature Operating	-10°C, 120 Hours	Finish product (With polarizer)
3	High temperature Storage	70°C, 200 Hours	Finish product (With polarizer)
4	Low temperature Storage	-20°C, 200 Hours	Finish product (With polarizer)
5	High temperature & humidity Storage	50°C, 90%RH, 120 Hours	Finish product (With polarizer)
6	Thermal Shock Storage (No operation)	-10°C , 30min.<=> 60°C , 30min. 10 Cycles	Finish product (With polarizer)
7	ESD test	Voltage:±8KV R:330 ohm,C:150pF Air discharge,10 times	Finish product (With polarizer)
8	Vibration test	10 => 55 =>10 => 55 => 10 Hz, within 1 minute;Amplitude:1.5mm. 15 minutes for each Direction (X,Y,Z)	Finish product (With polarizer)
9	Drop test	Packed, 100CM free fall 6 sides, 1 corner, 3edges	Finish product (With polarizer)

* Judgment after test: keep in room temperature for more than 2 hours.

- Current consumption < 2 times of initial value
- Contrast > 1/2 initial value
- Function: work normally

5.2 Inspection plan

Class	Item	Judgment	Class
Packing & Indicate	1.Outside and inside package	"Model no." , "lot no." and "quantity" Should indicate on the package.	Minor
	2.Model mixed and quantity	Other model mixed.....rejected. Quantity short or over....rejected.	Critical
	3.Product indication	"Model no." should indicate on the product	Major
Assembly	4.Dimension,LCD glass scratch And scribe defect	According to specification or drawing	Major
Appearance	5.Viewing area	Polarizer edge or LCD's sealing line is visible in the viewing arearejected	Minor
	6.Blemish 、 black spot 、 White spot in the LCD And LCD glass cracks	According to standard of visual inspection (inside viewing area)	Minor
	7. Blemish 、 black spot White spot and scratch on the polarizer	According to standard of visual inspection (inside viewing area)	Minor
	8.Bubble in polarizer	According to standard of visual inspection (inside viewing area)	Minor
	9.LCD's rainbow color	Strong deviation color (or Newton ring) of LCDrejected. Or according to limited sample (if needed, and inside viewing area)	Minor
	10.FPC	Burned area or wrong part number is on FPC. The symbol, character, and mark of FPC are unidentifiable. The stripped solder mask, A>1.0mm 0.3mm < stripped solder mask or visible circuit, A<1.0mm,and the number is ≧ 4 pieces. Particle between circuits in solder mask.. Circuit is peeled off or cracked. Any circuit risen or exposed. 0.2mm< Area of solder ball, A is ≧0.4mm,the number of solder ball is ≧ 3 pieces. The magnitude of solder ball, A is>0.4mm.	Minor

5.3 Standard of visual inspection

Class	Item	Judgment	Class
Electrical	11. Electrical and optical characteristics (contrast、VOP、chromaticity...etc)	According to specification or drawing. (inside viewing area)	Critical
	12. Missing pattern	Missing dot、line、character.....rejected	Critical
	13. Short circuit、wrong pattern display	Non display、wrong pattern display、current consumption out of specification.....rejected	Critical
	14. Pin hole、pattern deformity	According to standard of visual inspection	Minor
	15. Black spot、white spot、black line、white line、slant line、background uneven、color uneven	Strong deviation color.....rejected Or according to limited sample full off screen (all black) ...disregards	Minor
	16. Stick image (retention image)	Fixed test picture within two hours...rejected	Minor

Class	Item	Judgment																			
Minor	<ul style="list-style-type: none"> Blemish、black spot、white spot in the LCD. Blemish、black spot、white spot and scratch on th polarizer 	<p>(A) Round type: unit: mm</p> <table border="1"> <thead> <tr> <th>Diameter (mm.)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$0.2 < A$</td> <td>0</td> </tr> </tbody> </table> <p>Note: $A = (\text{Length} + \text{Width}) / 2$</p> <p>(B) Liner type: unit: mm</p> <table border="1"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$W \leq 0.03$</td> <td>Disregard</td> </tr> <tr> <td>$L \leq 5$</td> <td>$0.03 < W \leq 0.05$</td> <td>3</td> </tr> <tr> <td>$L \leq 5$</td> <td>$0.05 < W \leq 0.07$</td> <td>1</td> </tr> <tr> <td>---</td> <td>$0.07 < W$</td> <td>Follow round type</td> </tr> </tbody> </table>	Diameter (mm.)	Acceptable Q'ty	$0.2 < A$	0	Length	Width	Acceptable Q'ty	---	$W \leq 0.03$	Disregard	$L \leq 5$	$0.03 < W \leq 0.05$	3	$L \leq 5$	$0.05 < W \leq 0.07$	1	---	$0.07 < W$	Follow round type
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---	$0.07 < W$	Follow round type																			
Minor	Bubble in polarizer	<p>unit: mm</p> <table border="1"> <thead> <tr> <th>Diameter</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$A \leq 0.3$</td> <td>Disregard</td> </tr> <tr> <td>$0.3 < A \leq 0.5$</td> <td>1</td> </tr> <tr> <td>$0.5 < A$</td> <td>0</td> </tr> </tbody> </table>	Diameter	Acceptable Q'ty	$A \leq 0.3$	Disregard	$0.3 < A \leq 0.5$	1	$0.5 < A$	0											
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$0.5 < A$	0																				
Minor	Pin hole、Pattern deformity	<p>unit: dot size</p> <table border="1"> <thead> <tr> <th>Diameter</th> <th>Acc. Q'ty</th> </tr> </thead> <tbody> <tr> <td>$0.4 < \Phi$</td> <td>0</td> </tr> </tbody> </table>	Diameter	Acc. Q'ty	$0.4 < \Phi$	0															
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$0.4 < \Phi$	0																				

6. Precaution

6.1 Handling

- (1) Protect the panel from static, it may cause damage to the CMOS Gate Array IC.
- (2) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (3) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (4) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Don't use Ketone type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (5) Pins of I/F connector shall not be touched directly with bare hands.
- (6) Refrain from strong mechanical shock and / or any force to the panel. In addition to damage, this may cause improper operation or damage to the panel.
- (7) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a B pencil lead.
- (8) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining and discoloration may occur.
- (9) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

6.2 Storage

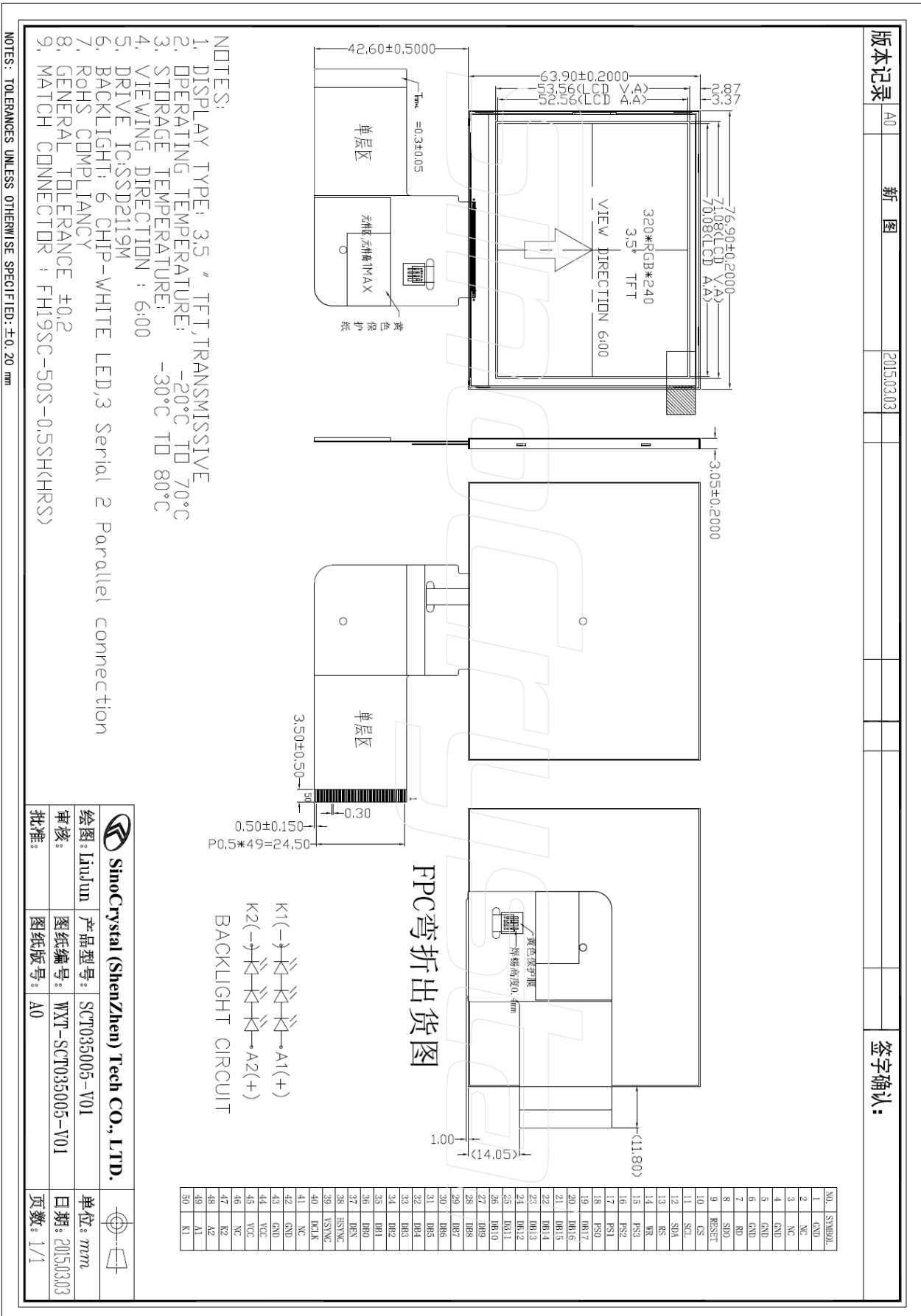
- (1) Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the panel with temperature from 0 to 35°C and relative humidity of less than 70%.
- (2) The panel shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

6.3 Operation

- (1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- (2) Do not exceed the absolute maximum rating value. (the supply voltage variation, Input voltage variation in part contents and environmental temperature and so on). Otherwise the panel may be damaged.
- (3) If the panel displays the same pattern continuously for a long period of time, it can be the situation when the image" Sticks" to the screen.

7. Outline Dimension

Refer to SCT035005-V01 drawing.



8. Packing method

8.1 Packing Quantity (TBD)

8.2 Flowing chart (TBD)

Sino Crystal