


SPECIFICATIONS FOR LCD MODULE

CUSTOMER	
MODEL	SCT101005-V02
CUSTOMER APPROVED	

APPROVED BY	CHECKED BY	ORGANIZED BY
	Lr.Yin	Wf.Luo

ADD : 6F. B block of 10 Building Huafeng Technology Park. Fengtang Road
Fuyong town Baoan district Shenzhen Guangdong

TEL : 0755-81452160

FAX : 0755-81452166



1. General Specifications

No.	Item	Specification	Remark
1	LCD size	10.1 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	1024× 3(RGB) × 600	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.2175(W) × 0.2088(H) mm	
6	Active area	222.72(W) × 125.28(H) mm	
7	Module size	235.0(W) × 143.0(H) × 5.0(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight power consumption	2.4W (Typ.)	
12	Panel power consumption	0.3W	
13	Weight	TBD	

Note 1: Refer to Mechanical Drawing.

2. Pin Assignment

FPC Connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.

Pin No.	Symbol	I/O	Function	Remark
1	V _{LED+}	P	Power for LED backlight (Anode)	
2	V _{LED+}	P	Power for LED backlight (Anode)	
3	V _{LED-}	P	Power for LED backlight (Cathode)	
4	V _{LED-}	P	Power for LED backlight (Cathode)	
5	GND	P	Power ground	
6	V _{COM}	I	Common voltage	
7	DVDD	P	Power for Digital Circuit	
8	MODE	I	DE/SYNC mode select	Note 1
9	DE	I	Data Input Enable	
10	VS	I	Vertical Sync Input	
11	HS	I	Horizontal Sync Input	
12	B7	I	Blue data(MSB)	
13	B6	I	Blue data	
14	B5	I	Blue data	
15	B4	I	Blue data	
16	B3	I	Blue data	
17	B2	I	Blue data	
18	B1	I	Blue data	Note 2
19	B0	I	Blue data(LSB)	Note 2
20	G7	I	Green data(MSB)	
21	G6	I	Green data	
22	G5	I	Green data	
23	G4	I	Green data	
24	G3	I	Green data	
25	G2	I	Green data	
26	G1	I	Green data	Note 2

27	G0	I	Green data(LSB)	Note 2
28	R7	I	Red data(MSB)	
29	R6	I	Red data	
30	R5	I	Red data	
31	R4	I	Red data	
32	R3	I	Red data	
33	R2	I	Red data	
34	R1	I	Red data	Note 2
35	R0	I	Red data(LSB)	Note 2
36	GND	P	Power Ground	
37	DCLK	I	Sample clock	
38	GND	P	Power Ground	
39	L/R	I	Left / right selection	Note 3,4
40	U/D	I	Up/down selection	Note 3,4
41	V _{GH}	P	Gate ON Voltage	
42	V _{GL}	P	Gate OFF Voltage	
43	AVDD	P	Power for Analog Circuit	
44	RESET	I	Global reset pin.	Note 5
45	NC	-	No connection	
46	V _{COM}	I	Common Voltage	
47	DITHB	I	Dithering function	Note 6
48	GND	P	Power Ground	
49	NC	-	No connection	
50	NC	-	No connection	

I: input, O: output, P: Power

Note 1: DE/SYNC mode select. Normally pull high.

When select DE mode, MODE="1", VS and HS must pull high.

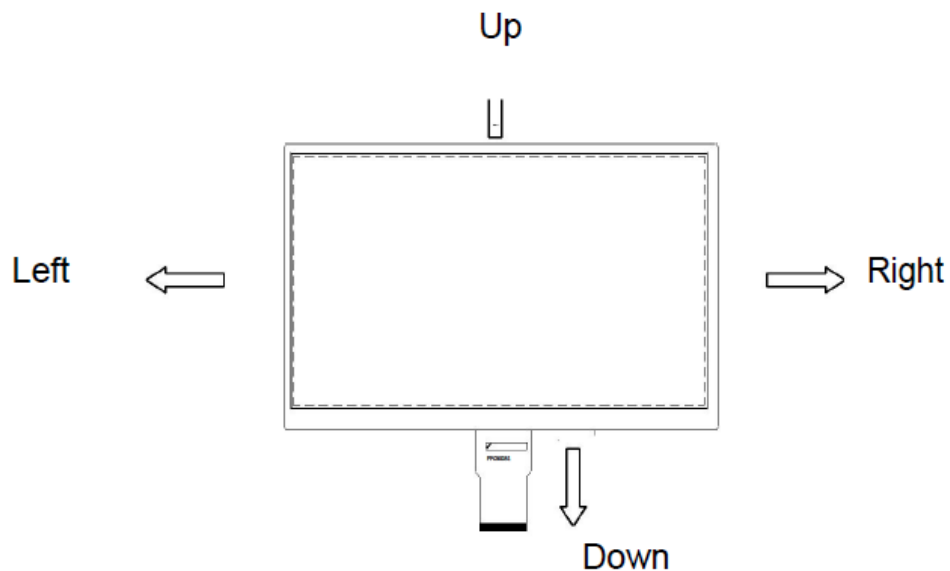
When select SYNC mode, MODE="0", DE must be grounded.

Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.

Note 3: Selection of scanning mode

Setting of scan control input		Scanning direction
U/D	L/R	
GND	DV _{DD}	Up to down, left to right
DV _{DD}	GND	Down to up, right to left
GND	GND	Up to down, right to left
DV _{DD}	DV _{DD}	Down to up, left to right

Note 4: Definition of scanning direction.
Refer to the figure as below:



Note 5: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

Note 6: Dithering function enable control, normally pull high.
When DITHB="1", Disable internal dithering function,
When DITHB="0", Enable internal dithering function,

3. Operation Specifications

3.1. Absolute Maximum Ratings

(Note 1)

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	DV_{DD}	-0.3	3.6	V	
	AV_{DD}	-0.3	13.0	V	
	V_{GH}	-0.3	30.0	V	
	V_{GL}	-15.0	0.3	V	
	$V_{GH}-V_{GL}$	-	30.0	V	
Operation Temperature	T_{OP}	-10	50	°C	
Storage Temperature	T_{ST}	-20	70	°C	
LED Reverse Voltage	V_R	-	1.2	V	Each LED Note 2
LED Forward Current	I_F	-	25	mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 2: V_R Conditions: Zener Diode 20mA

3.1.1. Typical Operation Conditions

(Note 1)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	DV _{DD}	3.0	3.3	3.6	V	Note 2
	AV _{DD}	9.2	9.6	11.0	V	
	V _{GH}	20.1	21.0	21.9	V	
	V _{GL}	-8.0	-7.0	-6.5	V	
Input signal voltage	V _{COM}	3.5	3.8	4.5	V	
Input logic high voltage	V _{IH}	0.7 DV _{DD}	-	DV _{DD}	V	Note 3
Input logic low voltage	V _{IL}	0	-	0.3 DV _{DD}	V	

Note 1: Be sure to apply DV_{DD} and V_{GL} to the LCD first, and then apply V_{GH}.

Note 2: DV_{DD} setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

3.1.2. Current Consumption

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Current for Driver	I _{GH}	-	0.5	1.0	mA	V _{GH} =21.0V
	I _{GL}	-	4.8	8.0	mA	V _{GL} = -7.0V
	ID _{VDD}	-	18.0	30	mA	DV _{DD} =3.3V
	IA _{VDD}	-	30	50	mA	AV _{DD} =9.8V

3.1.3. Backlight Driving Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED backlight	V _L	8.1	9.0	10.2	V	Note 1
Current for LED backlight	I _L		240		mA	
LED life time	-	20,000	-	-	Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and I_L =240mA.

Note 2: The “LED life time” is defined as the module brightness decrease to 50% original brightness at Ta=25°C and I_L =240mA. The LED lifetime could be decreased if operating I_L is larger than 240mA.

3.2. Power Sequence

HX8282-A01 has a power on/off sequence control function. In order to prevent IC from power on reset fail, the rising time (T_{POR}) of the digital power supply VDD should be maintained within the given specifications. Please refer to "AC Characteristics" for more detail on timing.

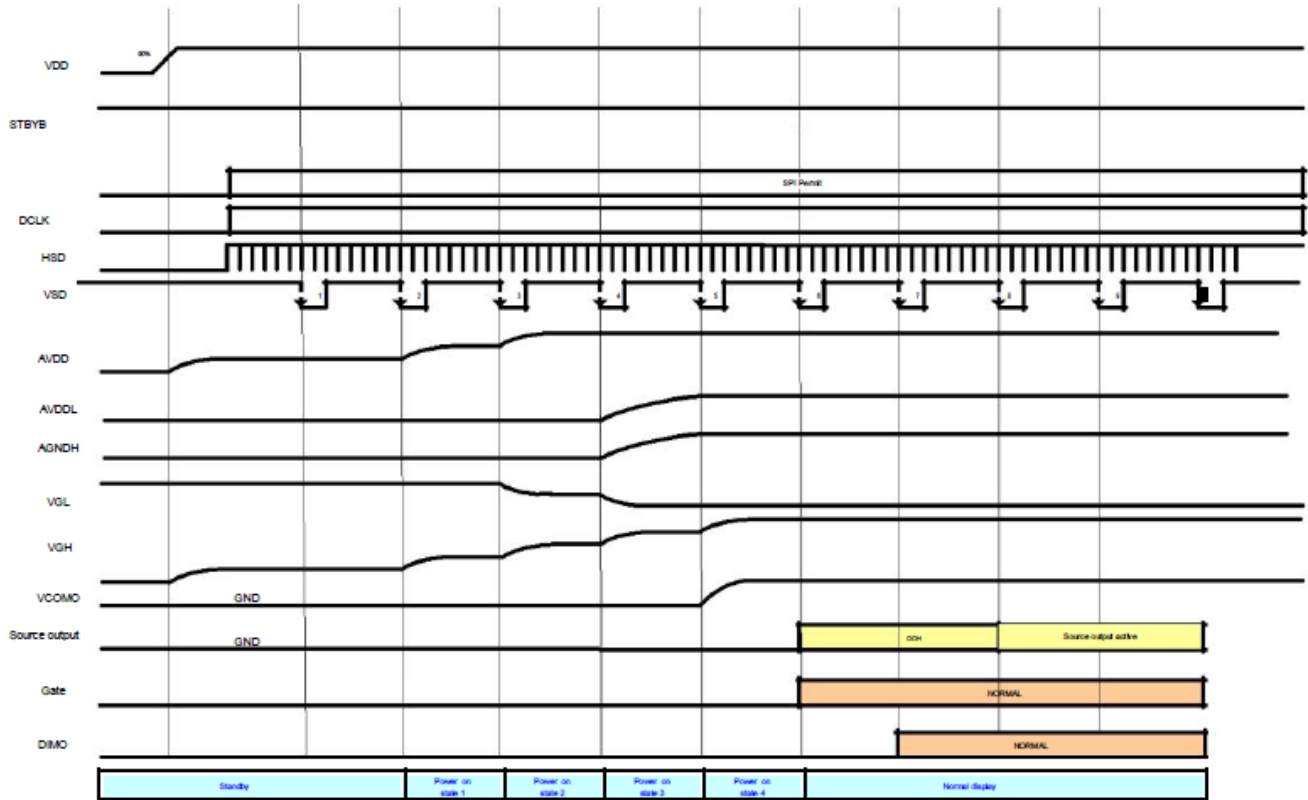


Figure 3.1: Power on timing sequence

DATA SHEET Preliminary V01

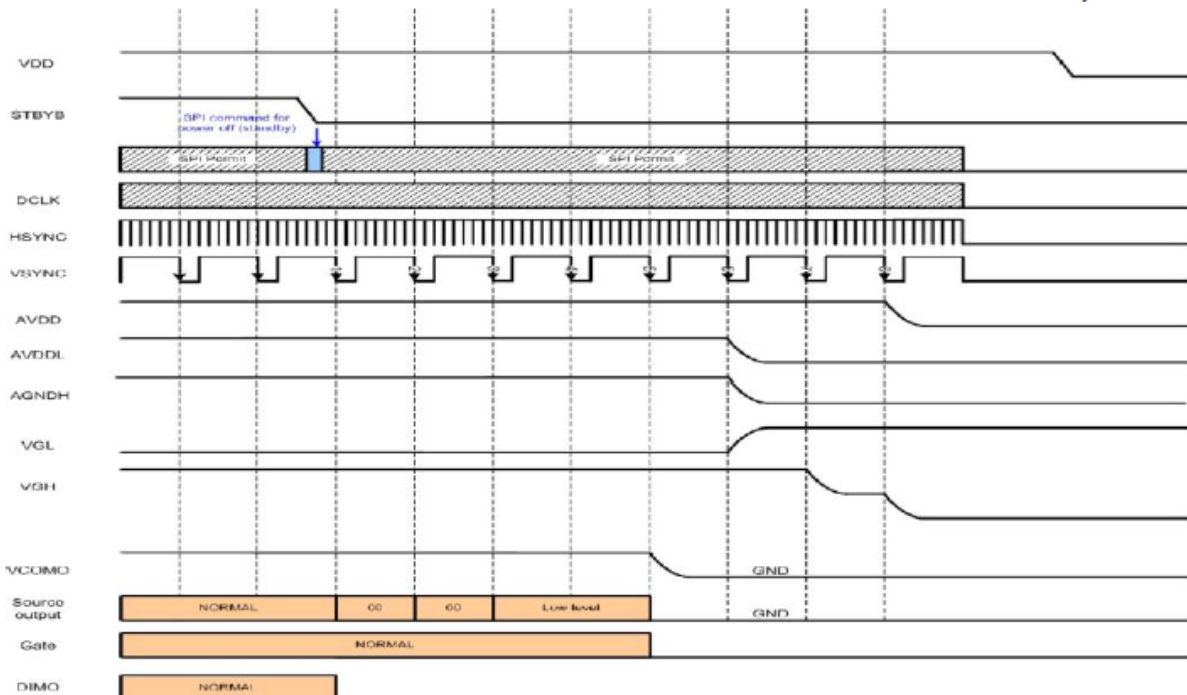


Figure 3.2: Power off timing sequence

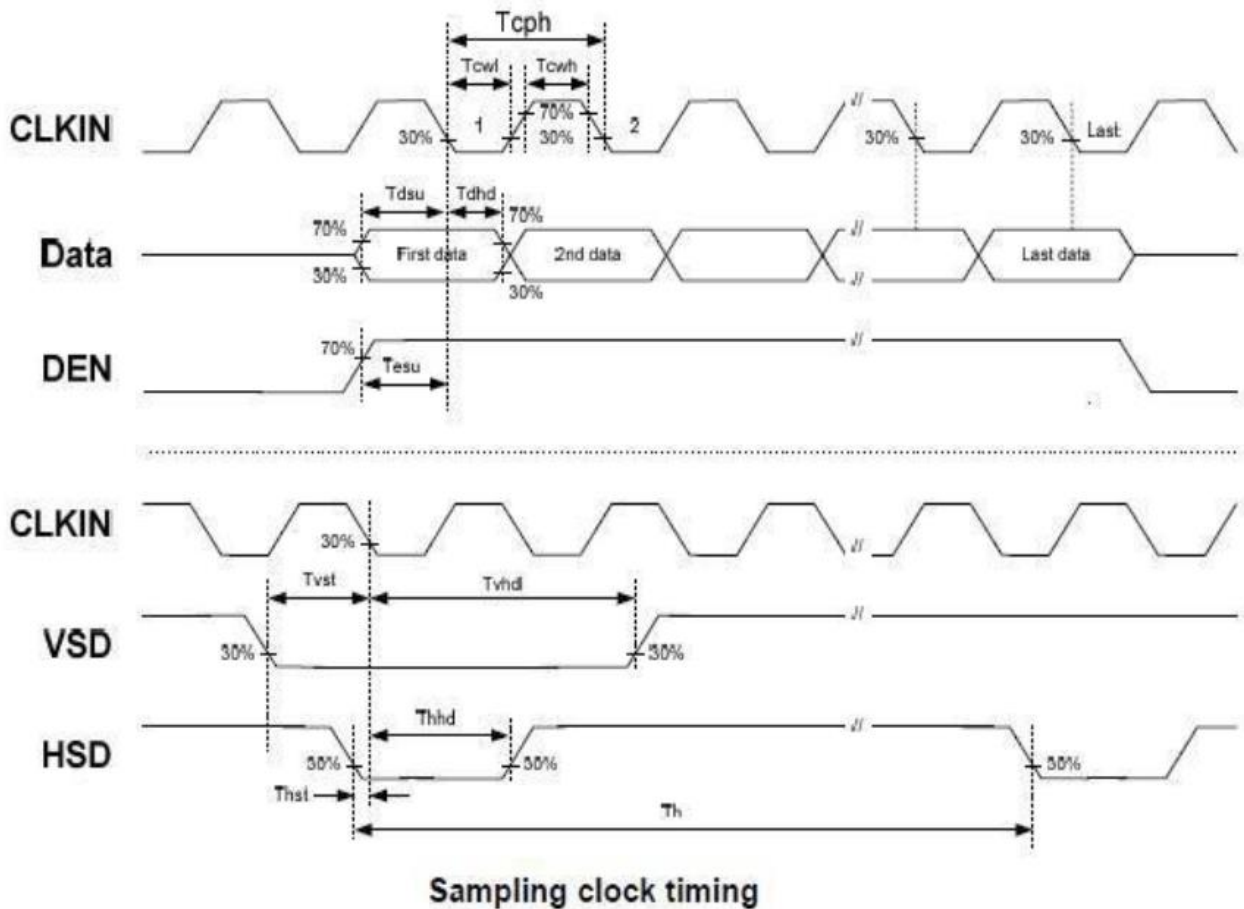
Note: Low level=3FH, when NBW=L (Normally white)
Low level=00H, when NBW=H (Normally black)

3.3. Timing Characteristics

3.3.1. AC Electrical Characteristics

Parameter	Symbol	Spec.			Unit	Condition
		Min.	Typ.	Max.		
DVDD Power On Slew Rate	TPOR	-	-	20	ms	From 0V to 90% DVDD
RSTB Pulse Width	TRst	50	-	-	us	DCLK=65MHz
DCLK Cycle Time	Tcph	14	-	-	ns	
DCLK Pulse Duty	Tcwh	40	50	60	%	
VSD Setup Time	Tvst	5	-	-	ns	
VSD Hold Time	Tvhd	5	-	-	ns	
HSD Setup Time	Thst	5	-	-	ns	
HSD Hold Time	Thhd	5	-	-	ns	
Data Setup Time	Tdsu	5	-	-	ns	D0[7:0],D1[7:0],D2[7:0] to DCLK
Data Hold Time	Tdhd	5	-	-	ns	D0[7:0],D1[7:0],D2[7:0] to DCLK
DEN Setup Time	Tesu	5	-	-	ns	
DEN Hold Time	Tehd	5	-	-	ns	

3.3.2. Input Clock and Data Timing Diagram



3.3.3. Timing

	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	Note
DE MODE	Dot Clock	1/tCLK	45	51.2	57	MHz	
	DCLK Pulse Duty	Tcwh	40	50	60	%	
	Horizontal Total Time	tH	1324	1344	1364	tCLK	
	Horizontal Effective Time	tHA	1024			tCLK	
	Horizontal Blank Time	tHB	300	320	340	tCLK	
	Vertical Total Time	tV	625	635	645	tH	
	Vertical Effective Time	tVA	600			tH	
	Vertical Blank Time	tVB	25	35	45	tH	
SYNC MODE	Horizontal Total Time	TH	1324	1344	1364	tCLK	
	Horizontal Pulse Width	Thpw		20	-	tCLK	t _{hb} + t _{h_{pw}} = 160DCLK is fixed
	Horizontal Back Porch	Thb		140	-	tCLK	
	Horizontal Front Porch	Thfp	140	160	180	tCLK	
	Horizontal Effective Time	THA	1024			tCLK	
	Vertical Total Time	TV	625	635	645	tH	
	Vertical Pulse Width	Tvpw		3	-	th	t _{vpw} + t _{vb} = 23th is fixed
	Vertical Back Porch	Tvb	-	20	-	th	
	Vertical Front Porch	Tvfp	2	12	22	th	
Vertical Valid	Tvd	600			th		

3.3.4. Data Input Format



Figure 3. 1 Horizontal input timing diagram.

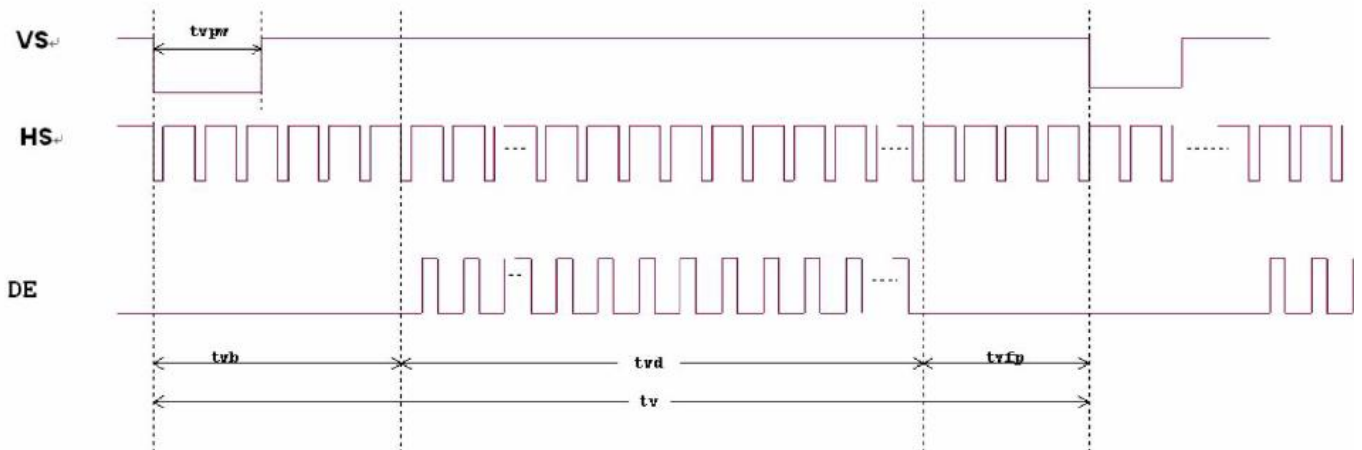


Figure 3. 2 Vertical input timing diagram.

4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥10)	θL	Φ=180°(9 o'clock)	60	70	-	degree	Note 1
	θR	Φ=0°(3 o'clock)	60	70	-		
	θT	Φ=90°(12 o'clock)	50	60	-		
	θB	Φ=270°(6 o'clock)	60	70	-		
Response time	TON	Normal θ=Φ=0°	-	10	20	msec	Note 3
	TOFF		-	15	30	msec	Note 3
Contrast ratio	CR		400	600	-	-	Note 4
Color chromaticity	WX		0.26	0.31	0.36	-	Note 2
	WY		0.28	0.33	0.38	-	Note 5 Note 6
Luminance	L		180	250	-	cd/m ²	Note 6
Luminance uniformity	YU		70	75	-	%	Note 7

Test Conditions:

1. DVDD=3.3V, IL=240mA (Backlight current), the ambient temperature is 25°C.
2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

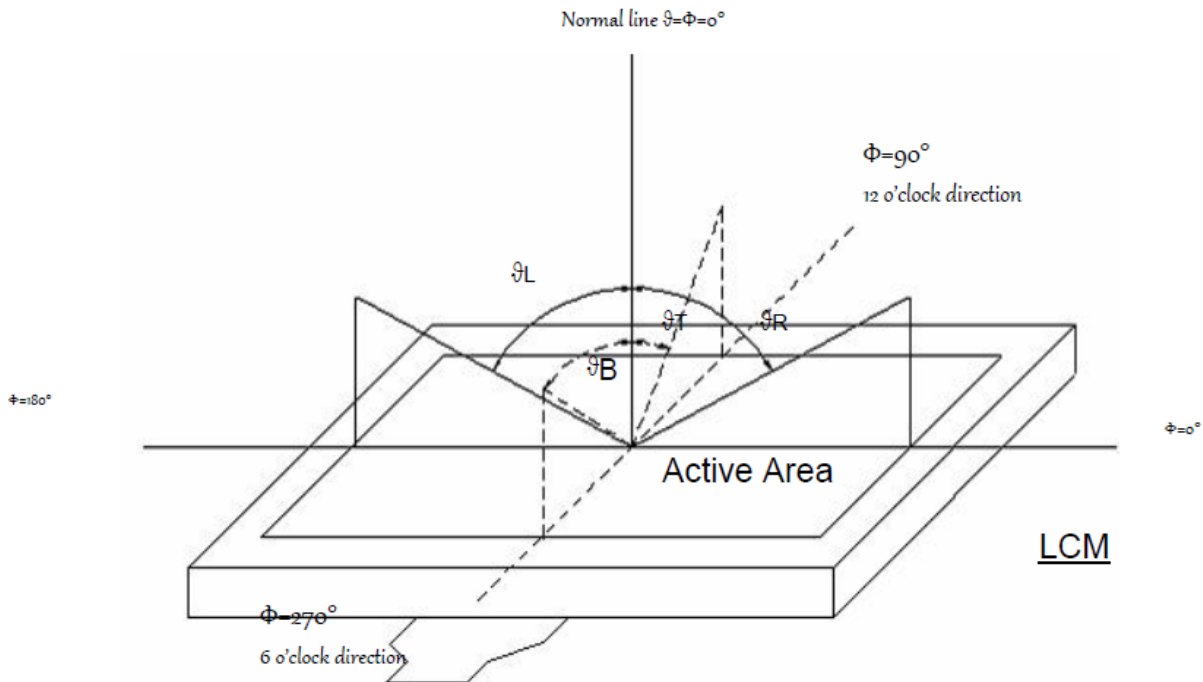


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

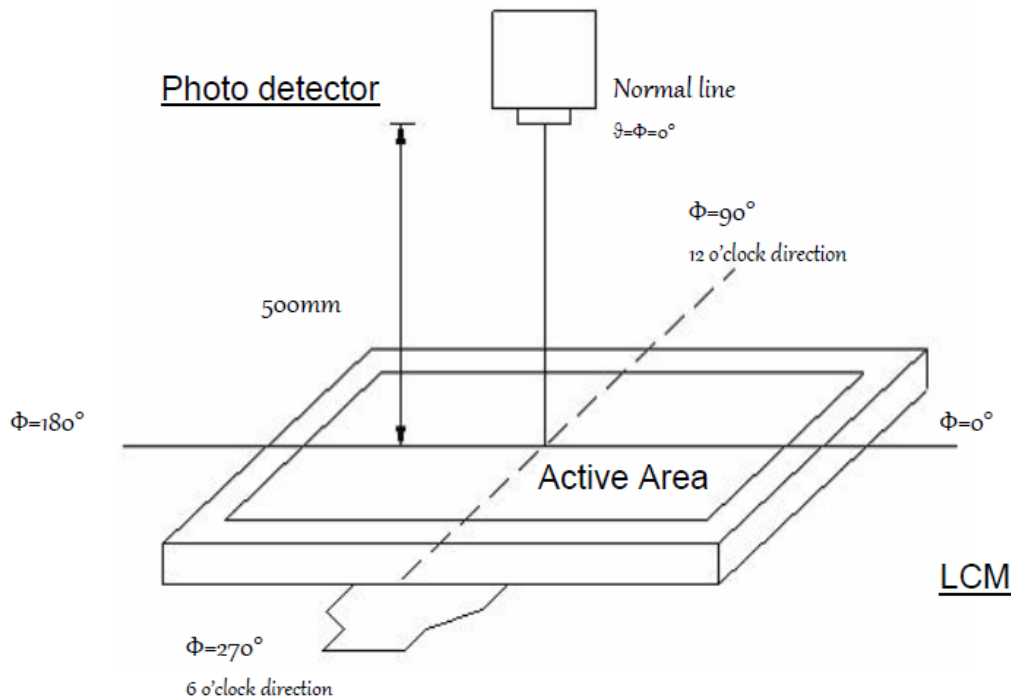


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

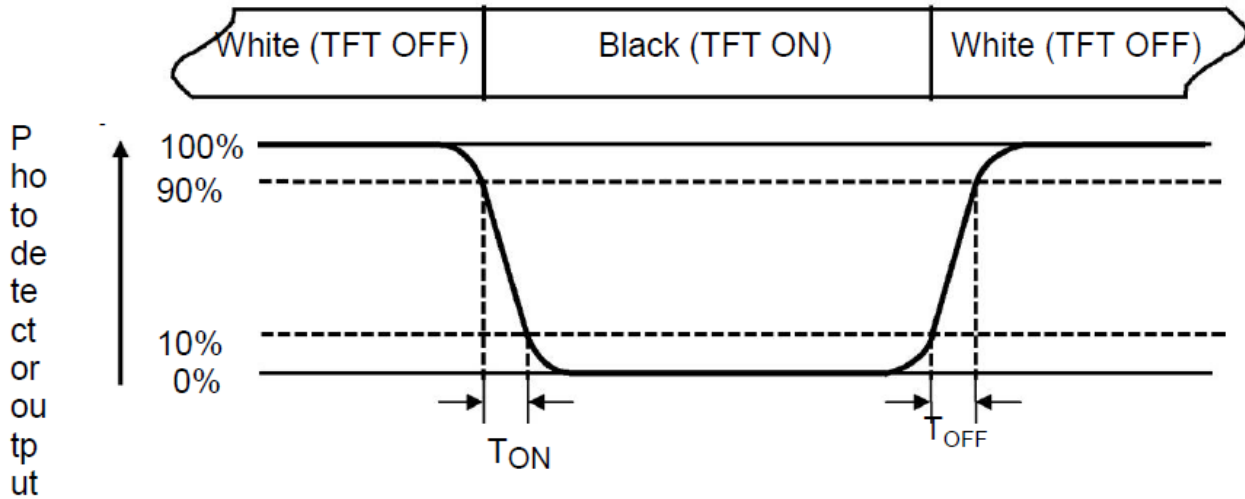


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $I_L=240\text{mA}$.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{\min}}{B_{\max}}$$

L-----Active area length W----- Active area width

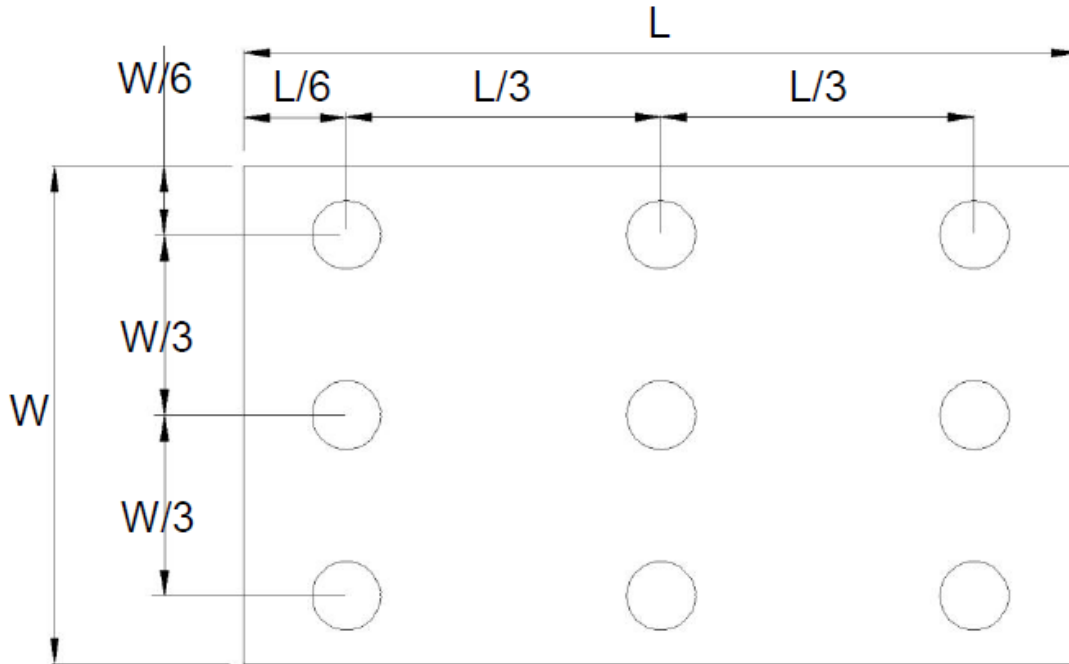


Fig. 4-4 Definition of measuring points

B_{\max} : The measured maximum luminance of all measurement position.

B_{\min} : The measured minimum luminance of all measurement position.

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6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

6.4. Storage

1. Store the module in a dark room where must keep at $25\pm 10^{\circ}\text{C}$ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

8. Packing method

8.1 Packing Quantity (TBD)

8.2 Flowing chart (TBD)

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